

WVS-VORTEX(1),1130/DCS-IOCS

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1  /LOAD,MIDAS * DECODER A
2  * DCSA CONTENTS FOR 1130 EMULATION
3  DCSA FORM 16,16,16,16
4  DCSA 1(0),2(0),3(0),4(0)
5  DCSA 1(1),2(1),3(1),4(1)
6  DCSA 1(2),2(2),3(2),4(2)
7  DCSA 1(3),2(3),3(3),4(3)
8  END
9  /LOAD,MIDAS * DECODER B
10 * DCSB CONTENTS FOR 1130 EMULATION
11 DCSB FORM 16,16,16,16
12 DJUMP EQU 0'00100 DECODER JUMP BASE ADDRESS
13 DBITS EQU X'E400 ENABLE DCSA LOW ORDER 9 BITS
14 DDEST EQU DBITS+DJUMP TOTAL DCSB CONTENTS
15 DBNOA EQU X'F000 DISABLE DECODER A
16 DDNOA EQU DBNOA+DJUMP DECODE USING B ONLY
17 DCSB 1(DDEST+00),2(DDNOA+04),3(DDEST+08),4(DDNOA+12)
18 DCSB 1(DDEST+16),2(DDNOA+20),3(DDEST+24),4(DDEST+28)
19 DCSB 1(DDEST+32),2(DDEST+36),3(DDEST+40),4(DDNOA+44)
20 DCSB 1(DDEST+48),2(DDEST+52),3(DDEST+56),4(DDEST+60)
21 END
22 /ASSIGN,BI,17
23 /PFILE,BI,,S1130
24 /ASSIGN,LO,LP
25 /FINI
26 /LOAD,MIDAS * I/O CONTROL STORE
27 * I/O CONTROL STORE CONTENTS FOR 620 AND 1130 EMULATION.
28 IOCS FORM 16,16,16,16
29 *
30 ORG 0 IDLE ENTERED VIA RESET KEY.
31 IOCS 1(0'000210),2(0'000000),3(0'000000),4(0'000000)
32 *
33 ORG 4 SEN, EXC, EXCA
34 IOCS 1(0'030210),2(0'000210),3(0'004210),4(0'006210)
35 IOCS 1(0'006211),2(0'000000),3(0'000210),4(0'000000)
36 *
37 ORG X'0C PROGRAMMED INPUT AND HALT LOOP
38 IOCS 1(0'030210),2(0'000210),3(0'004210),4(0'006210)
39 IOCS 1(0'006210),2(0'140210),3(0'140610),4(0'170600)
40 IOCS 1(0'000000),2(0'000210)
41 *
42 ORG X'1C PROGRAMMED OUTPUT AND HALT LOOP
43 IOCS 1(0'030200),2(0'000210),3(0'004210),4(0'006210)
44 IOCS 1(0'036210),2(0'004210),3(0'004610),4(0'004610)
45 IOCS 1(0'000000),2(0'000210)
46 *
47 ORG X'40 DMA OUTPUT
48 IOCS 1(0'140203),2(0'142200),3(0'172200),4(0'000346)
49 IOCS 1(0'130306),2(0'004200),3(0'004600),4(0'004602)
50 IOCS 1(0'000000),2(0'000210)
51 *
52 ORG X'50 DMA INPUT
53 IOCS 1(0'140203),2(0'142200),3(0'172200),4(0'000200)
54 IOCS 1(0'000200),2(0'140346),3(0'140200),4(0'140706)
55 IOCS 1(0'140602),2(0'000000),3(0'000210)
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57      ORG      X'60      HIGH SPEED DMA INPUT
58      IOCS     1(0'172343),2(0'000200),3(0'134703),4(0'004602)
59      IOCS     1(0'000000),2(0'000210)
60      *
61      ORG      X'70      HIGH SPEED DMA OUTPUT
62      IOCS     1(0'172343),2(0'140200),3(0'140703),4(0'000202)
63      IOCS     1(0'000000),2(0'000210)
64      *
65      ORG      X'DC      INTERRUPT
66      IOCS     1(0'140213),2(0'142210),3(0'172200),4(0'000300)
67      IOCS     1(0'000600),2(0'000602),3(0'000200),4(0'000200)
68      IOCS     1(0'000200),2(0'000000),3(0'000210)
69      *
70      ORG      X'FD      UNKNOWN FUNCTION
71      IOCS     1(0'140213),2(0'142210),3(0'172200),4(0'000300)
72      IOCS     1(0'000600),2(0'000602),3(0'000200),4(0'000200)
73      IOCS     1(0'000200),2(0'000000),3(0'000210)
74      END
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CPU:021 CTP:004 SUPS:911

@HDG,P

SOURCE DECK: 46:

1130/NEW-DCSB

@PRT,S PFFFILE,1130/NEW-DCSB  
FURPUR 27-S 08/16/78 10:13:22

WVS\*VORTEX(1),1130/NEW-DCSB

1	*	DCSB CONTENTS FOR 1130 EMULATION	001
2	DCSB	FORM 16,16,16,16	002
3	DJUMP	EQU X'20           DECODER JUMP BASE ADDRESS	003
4	DBITS	EQU X'E400           ENABLE DCSA LOW ORDER 9 BITS	004
5	DDEST	EQU DBITS+DJUMP       TOTAL DCSB CONTENTS	005
6	DBNOA	EQU X'F000           DISABLE DECODER A	006
7	DDNOA	EQU DBNOA+DJUMP       DECODE USING B ONLY	007
8	DCSB	1(DDEST+00),2(DDNOA+04),3(DDEST+09),4(DDNOA+05)	008
9	DCSB	1(DDEST+12),2(DDNOA+05),3(DDEST+16),4(DDEST+21)	009
10	DCSB	1(DDEST+06),2(DDEST+06),3(DDEST+06),4(DDNOA+05)	010
11	DCSB	1(DDEST+06),2(DDEST+06),3(DDEST+06),4(DDEST+24)	011
12		END	012

CPU:008 CTP:000 SUPS:704

@HDS,P

SOURCE DECK: 47:

1130/OLD

@PRT,S           PFFFILE.1130/OLD  
 FURPUR 27-S 08/16/78 10:13:24