

(900) 4 ORIG A-FIELD  
 (901) 0 (458) (678) ORIG B-FIELD  
 (908) 2

ABS 4 A2 6 5 RESULTANT A-FIELD  
 BI FINAL RESULTANT B-FIELD Q 0 2 6 5  
 ABS

SIGNAL NAME	FRAME GATE CH	TEST POINT	LOGIC	CYCLE																			
				I 7	A	B	A	B	A	B	B	B	I OP										
				3	6	9	0	3	6	9	0	3	6	9	0	3	6	9	0	3	6	9	0
1 STORAGE ADDRESS REGISTER	-		32.35.XX.2	908	458	678	457	677	456	676	675	674	908										
2 B-REGISTER	-		35.11.XX.2	WMC2	C41	C81	C42	2	WMA2	C21	4	AB41	WM2C										
3 A-REGISTER	-		35.11.XX.2	045-075																			
4 -T RESET ADD OP	O1B1		35.24.11.2	OP DECODE	A	B	2	4	T														
5 -T ARITH OPERATION	O1B1		35.27.11.2	4																			
6 -T GATED WORD MARK	O1B2		31.07.11.2	B WM					A WM				B WM										
7 -T I-E CHANGE	O1B2		31.05.31.2	6									6										
8 +U DELTA I CYCLE	O1B2		31.21.11.2	7	9								7	14									
9 -T I-CYCLE LATCH	O1B2		31.24.11.2	8									8										
10 +U I-STAR RESTORE GATE	O1A7		32.39.11.2	6	9																		
11 +U DELTA A-CYCLE	O1B2		31.22.11.2	7	9				RESET 13				24										
12 -T A-CYCLE LATCH	O1B2		31.25.11.2	11									12										
13 -T DELTA B-CYCLE LATCH	O1B2		31.23.11.2	12																			
14 +U B-CYCLE	O1B2		31.26.11.2	13																			
15 -T FIRST B-CYCLE TRIGGER	O1B3		34.31.13.2	5	6	9							14										
16 -T B-REGISTER ZONE INHIBIT	O1A3		35.10.21.2	9																			
17 -T B-REGISTER DIGIT INHIBIT	O1A3		35.10.31.2	9																			
18 -T B-REGISTER C-BIT INHIBIT	O1A3		35.10.21.2	9																			
19 -T B-REGISTER WORD MARK INHIBIT	O1A3		35.10.21.2	9																			
20 -T A-REGISTER DIGIT INHIBIT	O1A3		35.10.41.2																				
21 -T ARITHMETIC ZONE INHIBIT	O1A3		35.10.51.2																				
22 +U INHIBIT C-ZONE	O1A3		35.15.11.2																				
23 +U A-WORD MARK LATCH	O1B2		31.06.11.2																				
24 +U A-CYCLE ELIMINATE	O1B2		31.06.11.2																				
25 -T ARITHMETIC A-WORD MARK LATCH	O1A3		35.10.61.2																				
26 +U A-REG SET B-2-C	O1A3		35.10.61.2																				
27 +U MODIFIER CONTROL / 1	O1A8		32.42.11.2																				
28 +U MODIFIER CONTROL - 1	O1A8		32.42.11.2																				
29 -T MODIFIER CONTROL XFER	O1A8		32.42.41.2																				
30 +U MODIFIER OUTPUT ON BUS LINES	-		32.43.XX.2	909/908	457	677	456	676	455	675	674	673	909										
31 DATA ON INHIBIT DRIVE LINES	-		-	WM2C	CAB41	CB41	C24	C24	WMA2	C2	CB2	WM2	WMC2										