

653 A 701 A  
 654 B (701)  
 658 BLANK

1070K 23940

SIGNAL NAME	FRAME GATE CH	TEST POINT	LOGIC	CYCLE																				
				T4	T OP																			
				3	6	9	0	3	6	9	0	3	6	9	0	3	6	9	0	3	6	9	0	
1 STORAGE ADDRESS REGISTER			32.XX.XX.2	65R	701																			
2 B-REGISTER LATCHES			35.XX.XX.2	C-BIT	A																			
3 A-REGISTER LATCHES			35.XX.XX.2																					
4 -T BRANCH OPR	0151		35.26.11.2	OP DEC A	B 2 B 5 T																			
5 -T GATED WORD MARK	0182		31.07.11.2	B-WM OR 16																				
6 +U UNCOND. TRANSFER	0182		31.08.11.2	B-REG BLANK	030-000																			
7																								
8 +U DELTA T-CYCLE LATCH	0182		31.21.11.2	B-CYC LAT CNTL 7 9																				
9 -T T-CYCLE LATCH	0182		31.24.11.2	DELTA CYC LAT GT	8 000-060																			
10 +U T-STAR RESTORE GATE	01A7		32.39.11.2	5 8 1	NOT ALTER																			
11 +U EXECUTE ELIMINATE	0182		31.05.11.2	4 T-RING 4	060-090																			
12 +U TRANSFER PROGRAM SKIP	0186		34.21.41.2	T-RING 4 5																				
13 -T PROGRAM SKIP LATCH	0182		31.08.11.2	4 OR 12	17																			
14 +U T-A STAR GATE	0182		31.08.11.2																					
15 +U SET TO T-RING OP	0182		31.30.11.2																					
16 -T A-STAR GATE OUT	01A7		32.32.21.2	8 14 NOT SET-UP																				
17 -T T-RING OP TRG	0182		31.31.31.2	090-000	015-030																			
18																								
19 +U MODIFIER CONTROL +1	01A8		32.42.11.2																					
20 -T MODIFIER CONTROL XFER	01A8		32.42.41.2																					
21 MODIFIER OUTPUT ON BUS LINES			32.43.XX.2																					
22 DATA ON INHIBIT DRIVE LINES																								