

T-STAR & STAR R-STAR
 654 B (701) EFF T/E XFER 658 701 XXX
 658 P — — AFTER XFER 658 701 658
 701 H (336)

SIGNAL NAME	FRAME GATE CH	TEST POINT	LOGIC	CYCLE															
				I 4	T-B STAR	T OP	I 1												
				3 6 9 0	3 6 9 0	3 6 9 0	3 6 9 0	3 6 9 0	3 6 9 0	3 6 9 0	3 6 9 0	3 6 9 0	3 6 9 0	3 6 9 0	3 6 9 0	3 6 9 0			
1 STORAGE ADDRESS REGISTER			32.35.XX 32.36.XX	658	658	701	702												
2 B-REGISTER LATCHES			35.11.XX																
3 A-REGISTER LATCHES			36.16.XX																
4 -T BRANCH OPERATION	01B1		35.26.11																
5 -T GATED WORD MARK	01B2		31.07.11																
6 -T T/E CHANGE	01B2		31.05.11																
7 -T DELTA T-CYCLE LATCH	01B2		31.21.11																
8 -T T-CYCLE LATCH	01B2		31.24.11																
9 +U T-STAR RESTOR	01A8		32.39.11																
10 +U EXECUTE ELIMINATION	01A2		31.05.11																
11 +U TRANSFER PROGRAM SKIP	01B6		36.21.41																
12 -T PROGRAM SKIP LATCH	01B2		31.08.11																
13 +U T-A STAR GATE	01B2		31.08.11																
14 +U SET TO I RING OP	01B2		31.30.11																
15 -T T-B STAR 1	02B6		41.51.51																
16 +U T-B STAR TRIGGER	02B6		41.51.51																
17 +U T STAR R0	02B6		41.51.51																
18 -T T-B STAR 2 (B-STAR R1)	02B6		41.51.51																
19 +U T RING OP	01B2		31.31.11																
20 -T A-STAR GATE OUT	01A7		32.32.21																
21 -T T-STAR GATE OUT	01A7		32.34.21																
22 +U MODIFIER CONTROL + I	01A8		32.42.41																
23 -T MODIFIER CONTROL XFER	01A8		32.42.41																
24 MODIFIER OUTPUT ON BUS			32.43.XX	659	658	702	703												
25 DATA ON INHIBIT DRIVE				A	A	H	B												