

SIGNAL NAME	FRAME GATE CH	TEST POINT	LOGIC	I CYCLE								B CYCLE											
				I RING 0	I RING 1	I RING 2	I RING 3	I RING 4	I RING 5	I RING 6	I RING 7	I RING 8	FIRST	LAST	I RING 0								
B-REGISTER (WMT) BA (22)	-		35.21.11	3	6	9	0	3	6	9	0	3	6	9	0	3	6	9	0	3	6	9	0
•U MOVE OPERATION	01B1		35.25.11	[Timing diagram showing signal transitions]																			
B REGISTER (WMT) C B21 (L)	-		35.21.11	[Timing diagram showing signal transitions]																			
•U LOAD OPERATION	01B1		35.25.11	[Timing diagram showing signal transitions]																			
A REGISTER A06 (R)	-		70.11.21	[Timing diagram showing signal transitions]																			
•U UNIT SELECT LATCH. UNIT SELECT	02A2		70.11.21	[Timing diagram showing signal transitions]																			
-T TAPE +1 MODE	02A2		70.11.21	[Timing diagram showing signal transitions]																			
•U T/O OPS	01B2		31.09.21	[Timing diagram showing signal transitions]																			
•U A CYCLE ELIMINATION	01B2		31.06.11	[Timing diagram showing signal transitions]																			
A REGISTER BA2 (L) (ODD REDUNDANCY)	-		70.61.31	[Timing diagram showing signal transitions]																			
-T REDUNDANCY LATCH	02A2		70.61.31	[Timing diagram showing signal transitions]																			
•U UNIT SELECT GATE	02A2		70.11.21	[Timing diagram showing signal transitions]																			
A REGISTER 1,2,3,4,5, OR 6	-		71.11.11 71.11.31	[Timing diagram showing signal transitions]																			
•U SELECT TAPE UNIT (TAPE UNIT LATCH 1,2,3,4,5, OR 6)	02A1		71.11.11 71.11.31	[Timing diagram showing signal transitions]																			
•U GATE B REGISTER CONTENTS INTO THE B ADDRESS REGISTER	-			[Timing diagram showing signal transitions]																			
•U B REGISTER WORD MARK	01B2		31.07.11	[Timing diagram showing signal transitions]																			
•U GATED WORD MARK	G1B2		31.07.11	[Timing diagram showing signal transitions]																			
•U I CYCLE	01B2		31.05.31	[Timing diagram showing signal transitions]																			
-T T-E CHANGE	01B2		31.05.31	[Timing diagram showing signal transitions]																			
•U DELTA B CYCLE	01B2		31.23.11	[Timing diagram showing signal transitions]																			
-T B CYCLE	01B2		31.26.11	[Timing diagram showing signal transitions]																			
A REGISTER A02 (W)	-		70.11.21	[Timing diagram showing signal transitions]																			
•U WRITE CALL	02A2		70.11.21	[Timing diagram showing signal transitions]																			
-T CLOCK START CONTROL LATCH	02A1		71.31.21	[Timing diagram showing signal transitions]																			
-T TAPE CLOCK	02A1		71.31.21	[Timing diagram showing signal transitions]																			
•U CHECK CHARACTER (FROM TAU)	02A1		71.71.21	[Timing diagram showing signal transitions]																			
-T FORCED GROUP MARK	02A2		70.61.31	[Timing diagram showing signal transitions]																			
-T UNIT WRITE GATE	02A2		70.51.31	[Timing diagram showing signal transitions]																			
-T FORCED T/E CHANGE	02A2		70.11.41	[Timing diagram showing signal transitions]																			
•U SET TO I RING 0	01B2		31.30.11	[Timing diagram showing signal transitions]																			
•U DELTA I CYCLE	01B2		31.31.11	[Timing diagram showing signal transitions]																			
•U DELTA PROCESS	01B2		31.62.11	[Timing diagram showing signal transitions]																			
•U UNIT RESET	02A2		70.11.21	[Timing diagram showing signal transitions]																			
•U DISCONNECT	02A2		70.51.21	[Timing diagram showing signal transitions]																			
•P DISCONNECT CALL TO TAU	02A2		70.51.21	[Timing diagram showing signal transitions]																			

(SET WHEN IN ODD REDUNDANCY)

HUNDREDS GATE TENS GATE UNITS GATE

NOTE I

GATED BY WC 3 FROM TAU

NOTE II

NOTE I THERE IS NO 1401 TIMING RELATIONSHIP WITH THE TURN ON OF THIS LATCH.
NOTE II BETWEEN THE FIRST AND SECOND B CYCLE SEVERAL MILLISECOUNDS ELAPSE DURING WHICH TIME THE 1401 CLOCK DOES NOT RUN