

SIGNAL NAME	FRAME GATE CH	TEST POINT	LOGIC	CYCLE																							
				B	R	B	R	B	R	B	R	B	R	B	R												
				3	6	9	0	3	6	9	0	3	6	9	0	3	6	9	0	3	6	9	0				
1 -T DELTA B CYCLE LATCH	01B2		31.23.11	[Timing diagram for DELTA B CYCLE LATCH]																							
2 +U DELTA B CYCLE	01B2		31.23.11	[Timing diagram for DELTA B CYCLE]																							
3 -T B CYCLE	01B2		31.26.11	[Timing diagram for B CYCLE]																							
4 +U LOAD OPERATION	01B1		35.25.11	[Timing diagram for LOAD OPERATION]																							
5 DATA FROM TAPE	-		70.61.21	CHAR 1	WS CHAR	CHAR 2	CHAR 3	CHAR 4	CHAR 5	CHAR 6	CHAR 7	WS CHAR	CHAR 8	CHAR 9	[Timing diagram for DATA FROM TAPE]												
6 C REGISTER AN4 (WS CHAR)	-		70.61.21	[Timing diagram for C REGISTER AN4]																							
7 -T WS READ	02A2		70.61.31	[Timing diagram for WS READ]																							
8 +U TAPE READDRESS	02A2		70.51.31	[Timing diagram for TAPE READDRESS]																							
9 -T READDRESS LATCH (CY CONTROL)	01B2		31.04.11	[Timing diagram for READDRESS LATCH]																							
10 -T NOT READDRESS LATCH	02A2		70.51.31	[Timing diagram for NOT READDRESS LATCH]																							
11 +U INHIBIT WORD MARK	02A2		70.61.31	[Timing diagram for INHIBIT WORD MARK]																							
12	-			[Empty row]																							
13	-			[Empty row]																							
14 RESET B ADDRESS REGISTER	-		32.33.11	[Timing diagram for RESET B ADDRESS REGISTER]																							
15 GATE INTO B ADDRESS REGISTER FROM MODIFIER	-		32.33.11	[Timing diagram for GATE INTO B ADDRESS REGISTER]																							
16 GATE OUT THE B ADDRESS REGISTER	01A7		32.33.31	[Timing diagram for GATE OUT THE B ADDRESS REGISTER]																							
17 -T NOT ADDRESS REGISTER RESET	01B2		31.04.11	[Timing diagram for NOT ADDRESS REGISTER RESET]																							
18 GATE ADDRESS REG CONTENTS INTO MOD	-		32.39.21	[Timing diagram for GATE ADDRESS REG CONTENTS]																							

DATE CHANGE NO DATE CHANGE NO  
 4-10-62 11583C 31.7.62 TA 1370 K

INTERNATIONAL BUSINESS MACHINES CORP  
 NAME FIG 40 TIMING FOR LOAD TAPE AND READ A WORD SEPARATOR CHARACTER  
 DESIGN DETAIL CHECK APPRO  
 TYPE SCALE NONE  
 DRAW VE 1-26-62  
 CHECK