

SIGNAL NAME	FRAME GATE CH	TEST POINT	LOGIC	CYCLE																			
				B	B	B	B	B	B	B	B	B	B	B	B								
1 -T DELTA B CYCLE LATCH	01B2		31.23.11	3	6	9	0	3	6	9	0	3	6	9	0	3	6	9	0	3	6	9	0
2 +U DELTA B CYCLE	01B2		31.23.11	[Timing diagram showing pulse transitions]																			
3 -T B CYCLE	01B2		31.26.11	[Timing diagram showing pulse transitions]																			
4 DATA READ FROM STORAGE			70.51.21	[Timing diagram showing data read pulses]																			
5 +U LOAD OPERATION	01B1		35.25.11	[Timing diagram showing load pulses]																			
6 -T WRITE CALL	02A2		70.11.21	[Timing diagram showing write call pulses]																			
7 -T B REGISTER WORD MARK	02A2		70.51.21	[Timing diagram showing B register word mark pulses]																			
8 -T READDRESS LATCH	02A2		70.51.31	[Timing diagram showing readdress latch pulses]																			
9 +T NOT READDRESS LATCH	02A2		70.51.31	[Timing diagram showing not readdress latch pulses]																			
10 +U TAPE READDRESS	02A2		70.51.31	[Timing diagram showing tape readdress pulses]																			
11 -T READDRESS LATCH (CY CONTROL)	01B2		31.04.11	[Timing diagram showing readdress latch (CY CONTROL) pulses]																			
12 -T WS ON TAPE	02A2		70.51.31	[Timing diagram showing WS ON TAPE pulses]																			
13 FORCE LINES A,B,C, I TO TAU			70.51.21	[Timing diagram showing force lines pulses]																			
15 RESET B ADDRESS REGISTER			32.33.11	[Timing diagram showing reset B address register pulses]																			
16 GATE INTO B ADDRESS REGISTER FROM MODIFIER			32.33.11	[Timing diagram showing gate into B address register pulses]																			
17 -T GATE OUT THE B ADDRESS REGISTER	01A7		32.33.31	[Timing diagram showing gate out the B address register pulses]																			
18 -T NOT ADDRESS REGISTER RESET	01B2		31.04.11	[Timing diagram showing not address register reset pulses]																			
20 GATE ADDRESS REG CONTENTS INTO MOD AND INTO STORAGE				[Timing diagram showing gate address reg contents pulses]																			