



**Magnetic Core Shift Register SR3**

The FZ -- card is a single magnetic core position used to store an information bit. This shift register card consists of four input windings, a read-out winding, and three output windings.

The input windings are driven by core mode Z lines from either a capacitor storage network or from a special bit insert driver. These input windings are gated on by selecting the read-in driver circuits (HG -- or CZ -- card).

Normally, all reset windings of a shift register are serially connected and receive a constant current read-out pulse from a read-out control driver (CY -- card). The output windings are biased by a read-out control driver (HF -- card), and control the status of the output transistors.

The serial output at pin Q allows the FZ -- card to serial shift or regenerate information bits in the register position. The Bus 1 and Bus 2 outputs at pins P and R allow parallel transfer of information to either of two other registers. Shift register operation for this card is similar to that for the FX -- card.

**Application**

The FZ -- card allows additional flexibility in designing magnetic core shift registers. It provides a core position that can be set by any one of several inputs without using an extender card. This card also permits parallel read-out to 1 of 2 bus lines along with the normal serial transfer and regeneration feature provided by the FX -- card.