

# Appendix A. SMS Card Index by Function

Function	Sym- bol	Card Code	Ckts per Card	Inputs per Ckt	Internal Loading per Card	Application Notes
<b>1. CURRENT MODE BASIC LOGIC</b>						
Logic	+A	AA--	2	2	None	
		ZU	2	2	Out $\emptyset$	
		ZV	2	2	In $\emptyset$	
		ZW	2	2	All	
		ACYX	1	3	None	
		YY	1	3	Out $\emptyset$	
		YZ	1	3	In $\emptyset$	
		ZA	1	3	All	
		ACZF	1	4	None	
	ZG	1	4	Out $\emptyset$		
	ZH	1	4	In $\emptyset$		
	ZJ	1	4	All		
	+O	AB--	2	2	None	
		ZU	2	2	Out $\emptyset$	
		ZV	2	2	In $\emptyset$	
		ZW	2	2	All	
		ADYX	1	3	None	
		YY	1	3	Out $\emptyset$	
YZ		1	3	In $\emptyset$		
ZA		1	3	All		
ADZF		1	4	None		
ZG	1	4	Out $\emptyset$			
ZH	1	4	In $\emptyset$			
ZJ	1	4	All			
Extenders	E	ACYV	1	4		Extends N Blocks
		YW	1	2		Extends P Blocks
		ADYV	1	4		
		YW	1	2		
Converters	C	AMZX	2	1	None	N to P
		ZY	2	1	Out $\emptyset$	
		ZZ	2	1	In $\emptyset$	
		--	2	1	All	
	C	ANZX	2	1	None	P to N
		ZY	2	1	Out $\emptyset$	
		ZZ	2	1	In $\emptyset$	
		--	2	1	All	
AQ--	4	1	All	CM to VM		
<b>2. CTDL BASIC LOGIC</b>						
Logic	+A	CK--	2	3	0	Additional Extender
		VU	2	3	1	
		WV	2	3	2	
		YC	2	3	2	
	-A	CH--	3	2	0	
		VV	3	2	1	
		VW	3	2	2	
		WW	3	2	3	
Logic	+A	CJ--	2	3	0	Additional Extender
		VU	2	3	1	
		WV	2	3	2	
		YC	2	3	2	
	-A	CG--	3	2	0	
		VV	3	2	1	
		VW	3	2	2	
		WW	3	2	3	
Logic	-A	JG--	3	2	0	High Speed
		VV	3	2	1	
		VW	3	2	2	
		WW	3	2	3	
Logic	-A	JH--	2	3	0	High Speed
		VU	2	3	1	
		WV	2	3	2	
		WW	2	3	2	
Extender	E	CLVQ	3			See Card for Application Data
		VR	4			
		VS	3			
		VT	4			
Converters	C	CQ--	4	1	0	T to U
		YG	4	1	1	
		ZT	4	1	2	
		ZV	4	1	4	
		CR--	4	1	0	
YG	4	1	1	U to T		

Function	Sym- bol	Card Code	Ckts per Card	Inputs per Ckt	Internal Loading per Card	Application Notes	
Converters	C	CRZT	4	1	2	U to T	
		ZV	4	1	4		
		JF--	4	1	0	T to U	
		VA	4	1	1	High Speed	
		VN	4	1	2		
		VP	4	1	4		
		JJ--	4	1	0	U to T	
VA	4	1	1	High Speed			
VN	4	1	2				
VP	4	1	4				
<b>3. DIFFUSED JUNCTION BASIC LOGIC</b>							
Logic	+A	DEZJ	1	4	All	Type B	
		ZH	1	4	In $\emptyset$		
		ZG	1	4	Out $\emptyset$		
		ZF	1	4	None		
		DEZA	1	3	All		Type B
		YZ	1	3	In $\emptyset$		
		YY	1	3	Out $\emptyset$		
		YX	1	3	None		
		DBZW	2	2	All		No Out $\emptyset$ Load on 2nd Ckt
		ZV	2	2	In $\emptyset$		
		ZU	2	2	Out $\emptyset$		
		ZT	2	2	None		
	ZD	2	2	All			
	YD	2	2	All			
	DEZN	1	4	All	Type A		
	ZL	1	4	Out $\emptyset$			
	DEZE	1	3	All	Type A		
	ZC	1	3	Out $\emptyset$			
	DAZW	2	2	All	Type A		
	ZU	2	2	Out $\emptyset$			
	+O	+A	DFZJ	1	4	All	Type B
			ZH	1	4	In $\emptyset$	
			ZG	1	4	Out $\emptyset$	
			ZF	1	4	None	
DFZA		1	3	All	Type B		
YZ		1	3	In $\emptyset$			
YY		1	3	Out $\emptyset$			
YX		1	3	None			
DDZW		2	2	All	Type B		
ZV		2	2	In $\emptyset$			
ZU		2	2	Out $\emptyset$			
ZT		2	2	None			
DFZN	1	4	All	Type A			
ZL	1	4	Out $\emptyset$				
DFZE	1	3	All	Type A			
ZC	1	3	Out $\emptyset$				
DCZW	2	2	All	Type A			
ZU	2	2	Out $\emptyset$				
Exclusive Or	+OE	DBZS	1	4	All		
		ZR	1	4	In $\emptyset$		
	ZQ	1	4	Out $\emptyset$			
	ZP	1	4	None			
-OE	DDZS	1	4	All			
	ZR	1	4	In $\emptyset$			
ZQ	1	4	Out $\emptyset$				
ZP	1	4	None				
Converters	C	DBZZ	2	1	All	Type B (N to P)	
		ZY	2	1	In $\emptyset$		
		ZX	2	1	Out $\emptyset$		
		--	2	1	None		
		DAZZ	2	1	All	Type A (N to P)	
		ZX	2	1	Out $\emptyset$		
		DDZZ	2	1	All	Type B (P to N)	
		ZY	2	1	In $\emptyset$		
		ZX	2	1	Out $\emptyset$		
		--	2	1	None		
DCZZ	2	1	All	Type A (P to N)			
ZX	2	1	Out $\emptyset$				
Converter Buffer Terminator	CBT	DEYR	1	1	All	N to P	
		YS	1	1	Out $\emptyset$		
		DFYR	1	1	All		
YS	1	1	Out $\emptyset$	P to N			