

Appendix A (continued)

Function	Sym- bol	Card Code	Ckts per Card	Inputs per Ckts	Internal Loading per Card	Application Notes	
Converter Buffer	CB	DEYU	1	1	All	N to P	
		DEYT	1	1	Out \emptyset		
		DFYU	1	1	All	P to N	
		DFYT	1	1	Out \emptyset		
Extender	E	DEYV	1	4		Extends N Block	
		DEYW	1	4			
		DFYV	1	4		Extends P Block	
		DFYW	1	4			
4. SPECIAL FUNCTIONS							
Amplifiers	AM	GG--				Capacitor Sense	
		HB--					
		VV VW WW					
		GU--				Drum Sense	
		HH--				Tape Core Sense	
Capacitor Storage	CS	GB-- HY-- HZ--					
Converters	C	CNWU CPWU GW-- HAVE HAVF				U to T T to U U to X, X to U Z to N Z to U and T	
Coupling Networks	R	AG-- AK-- CM-- GF-- QS--				Line Driver Loads Logic Block Loads CM to CTDL Loads Core Buffer Loads Drum Wr. Driver Ld.	
Emitter Follower	DE	AEWY AEWZ CNWT CPWT				N Output P Output T Output U Output	
Delay Circuits	DLY	KH-- KJ-- KK-- KL-- KM-- KN-- KP--				This Group of Cards Provides 7 to 30,000 us Delay of the Start of the Output Signal	
Drivers	D	FU--				Gate and Reset Drum Write Coil Latch P and N Output	
		GS--					
		HR--					
		KE--					
	DC	GLVG					Inverter(Core Driver) Emitter Follower Bit Insert
		GLVH					
		GX--					
		GY--					
		GZ--					
		HF--					
		HG-- HJ--					
	DI	EA--					Indicator " " " "
		EB--					
GR--							
HX-- KA--							
DP	DMYP					Power(4-10 Bases) (4-10 Bases) (11-40 Bases) (11-40 Bases)	
	DMYQ						
	DNYJ						
	DNYL LE--						
DR	GLVJ					Relay Driver	
DS	GT--					Drum Shaper	

Function	Symbol	Card Code	Application Notes	
Integrators	C	AQ--	CM to VM	
		BGWA	W to N and P	
		WB	W to N and P	
		WC	W to N	
		WD	W to P	
		GP--	W to U	
		KQ--	W to T	
	CBL	GQ--	V to V	
	R	HK--	Core Load (V to V)	
Inverters	I	JLVB	Logic U to U	
		JMVB	Logic T to T	
	IP	CY-- HE-- HS-- KB--	T to U N to U Coil Drivers P to T	
Line Drivers	DL	AMWX	N to P	
		ANWX	P to N	
		CS--	T to P	
		CT--	U to N	
		EF--	N to N	
		EG--	P to P	
		HC--	V to V	
		KS-- KT--	P to P N to N	
Line Terminators	DT	AF--	N to P	
		AL--	P to N	
		CU--	N to U	
		CV--	P to T	
		DH--	N to P	
		DJ--	P to N	
		DK--	P to P	
		DL--	N to N	
		HD--	N to P	
		Magnetic Core Cards	SR	FX--
FY--	" " Extender			
FZ--	" " -3 Outputs			
GA-- KD--	" " Extender (Significant Detection)			
	VCK	GH--	Validity Check	
	RDB	GM-- GN--	I/O Core Buffer " " "	
Oscillator	OSC	GV--	Variable Gated (75KC-95KC)	
Triggers	SS	AW--	CM "P" Out Single Shot	
		EHVK	Diffused Junction P and N Out	
		VL		
		VM		
		EJVK	Diffused Junction N and P Out	
		VL		
		VM		
			NB--	CTDL "T" Out Single Shot
			NC--	CTDL "U" Out Single Shot
		T	CW-- JZ--	CTDL U and T Outputs CTDL U and T Outputs (Ext. Input)
	+TB	FG-- FH--	Binary Trigger N Out	
	-TB	FF-- FJ--	Binary Trigger P Out	
Special Purpose	FILT CAP DLY E BD	GJ--	Filter Card for Supply Voltages	
		GK--	Cable Decouple Capacitors	
		HQ--	Capacitors used for Ring Delay	
		JN--	Trigger Input Extender JZ--	
		LU--	Bus Capacitor Storage Discharge	