7234-61

TAU LOGIC	SIGNAL NAME	O2A1 LOGIC
B9.30.52.1	+Y WD 52	71.31.11.2
B9.30.52.1	+Y WD 80	71.31.11.2
B9.30.55.1	+Y WD 320 OR WD 1080	71.31.11.2
B9.60.30.1	+Y WRITE COND	71.31.11.2
B9.60.51.1	+Y SEL +T1 ON	71.31.11.2
B9.50.50.1	-Y ERROR	71.31.11.2
B9.60.02.1	+Y BUSY	71.31.11.2
B9.30.56.1	+Y FWD STOP DELAY	71.31.11.2
B9.20.30.1	-Y WC 2	71.31.21.2
B9.10.31.1	-Y WR RC-5 OR RD RC 7	71.31.21.2
B9.60.02.1	-Y LOAD POINT	71.31.21.2
B9.40.52.1	+Y R-W REG C-BIT	71.61.11.2
B9.40.51.1	+Y R-W REG B BIT	71.61.11.2
B9.40.51.1	+Y R-W REG A BIT	71.61.11.2
B9.40.51.1	+Y R-W REG 8 BIT	71.61.11.2
B9.40.50.1	+Y R-W REG 4 BIT	71.61.11.2
B9.40.50.1	+Y R-W REG 2 BIT	71.61.11.2
B9.40.50.1	+Y R-W REG 1 BIT	71.61.11.2
B9.40.61.1	-Y CHECK CHAR	71.71.21.2
B9.20.30.1	-Y WC 8 TR	71.71.41.2
B9.30.52.1	+Y WD 49	71.61.21.2
B9.20.10.1	-Y 729 HI DEN 800 OR 556 BPI	71.61.21.2
B9.10.20.1	+Y RC 4 TR	71.61.21.2
B9.10.18.1	+Y SEL + RDY HI 7330	71.61.21.2
B9.60.31.1	+Y WRITE TM DRIVE	71.31.21.2
B9.30.10.1	+Y END READ DELAY	71.61.21.2
B9.10.10.1	-Y SEL + RDY MOD 4	71.61.21.2

NOTE: A 'Y' LINE IS CONSIDERED
A '1' IN STBL

DATE 3.7.62	DATE 7.2.62	DATE	EC NO	DATE	EC NO	DATE	EC NO
TA 1370B	TA 1715A	9-7-61	110452 F	11-26-62	113835	4-10-63	114613
		11-3-62	113635				

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1715 A