

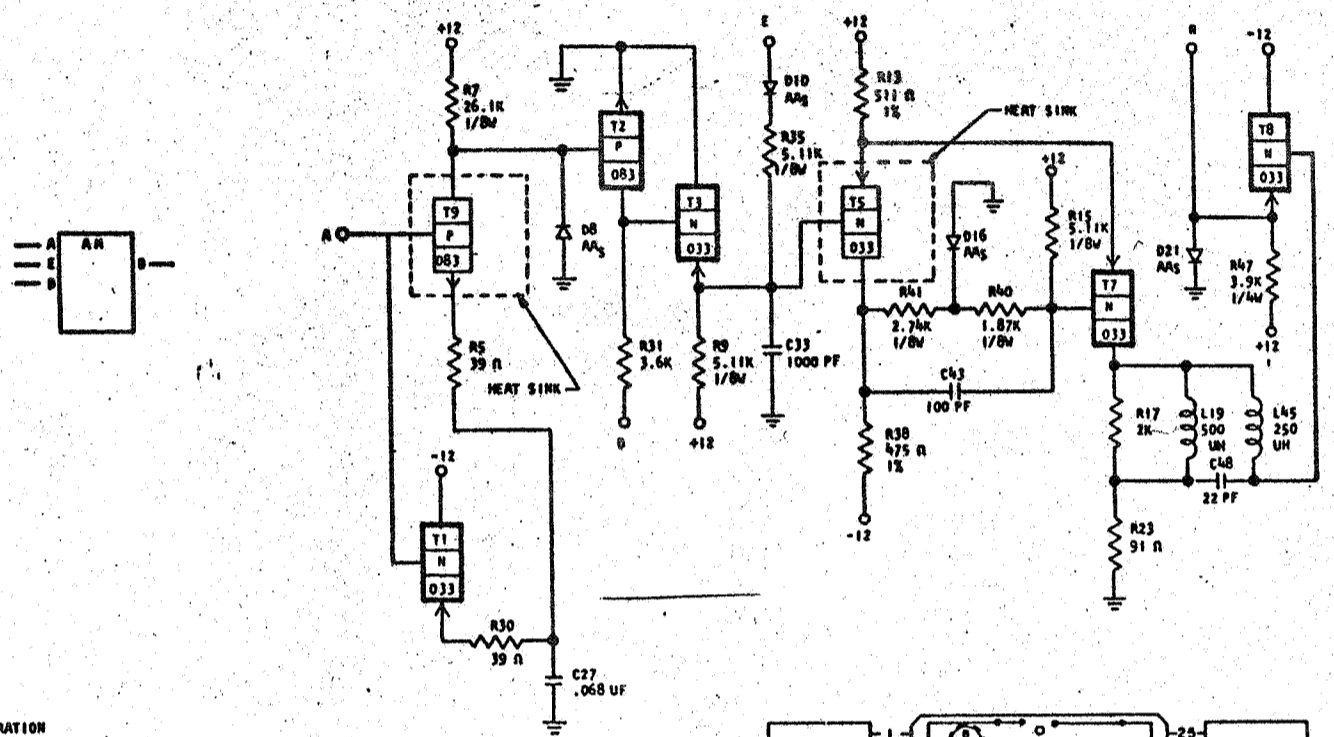
729951

STANDARD CODE

CARD CODE 729951
VCC -

REFERENCE DRAWING
SEE PRODUCTION DRAWING 370421

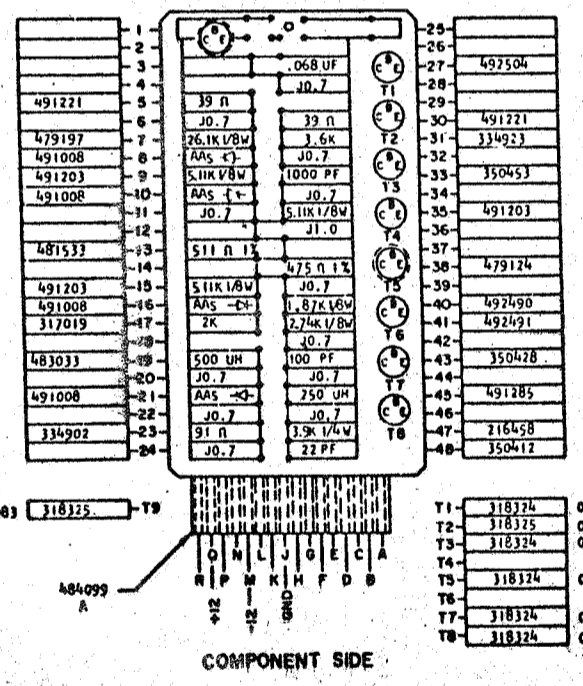
PEAK DETECTOR, INTEGRATOR & V.M. DRIVER



SEQUENCE OF OPERATION

1. THIS CIRCUIT IS USED TO SENSE A PEAK IN THE TAPE SIGNAL AFTER IT HAS BEEN AMPLIFIED BY THE TAPE PRE-AMPLIFIER. TRANSISTORS T1 AND T2 SENSE THE PEAK AND THIS SIGNAL IS AMPLIFIED BY T2 TO DRIVE T3 WHICH IN TURN DRIVES THE INTEGRATOR AND THE SCHMITT TRIGGER. WHEN THE SCHMITT TRIGGER FIRES, THE FALL TRANSITION IS SHAPED INTO THE OUTPUT PULSE (-12V) AND THEN IT IS COUPLED TO THE LOAD BY T8, AN EMITTER FOLLOWER.
2. A READ CONTROL LINE IS USED TO GATE THE SIGNAL DURING SWITCHING BETWEEN READ AND READ-WHILE-WRITE CONDITIONS; PIN D, ANOTHER CONTROL LINE CHANGES THE NOISE REJECTION OF THE CIRCUIT DEPENDING UPON THE TYPE OF TAPE DRIVE WHICH IS USED; PIN E, FOR HIGH FREQUENCY OPERATION THE INPUT AT PIN E IS AT +12 V, FOR LOW FREQUENCY OPERATION THE INPUT TO PIN E IS 0 V.
3. OUTPUT CAN DRIVE SDTDL OR SDTRL CIRCUITS.

PINS	SIGNAL NAME	WAVE SHAPE	LEVELS	
			MIN	MAX
A	INPUT		UP	9 PEAK
E	NOISE REJECTION GATE		UP	0
			DOWN	+10.41 +12.48
B	OUTPUT		UP	0 +.4
D	READ GATE		UP	+9.39 +12.48
			DOWN	0 +.7



CIRCUIT AND PACKAGING STANDARD	
APPROVAL	DATE
ABC	4-2-62

INTERNATIONAL BUSINESS MACHINES CORP.	DATE	CHANGE NO.	APPROVAL	DATE	CHANGE NO.	APPROVAL	DEVELOPMENT NO.
NAME CARD ASM TSTR - PEAK	6-27-62	EC-115599					729951
DETECTOR INTEGRATOR & V.M. DRIVER	30.4.63	JT 83687					
DESIGN	RD	3-1-62	SCALE	NONE			
CHECK	WH	3-1-62	DRAW	LEG	3-17-62		
APPROV			CHECK				

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