

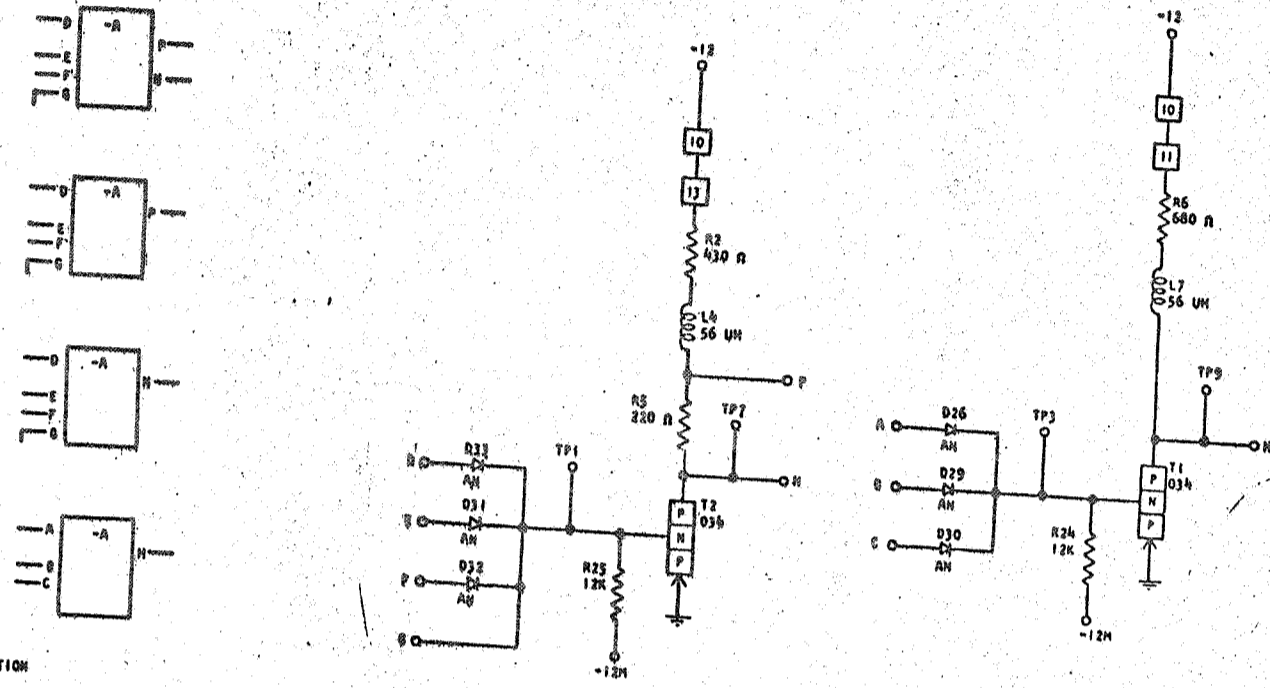
729874

STANDARD CODE

CARD CODE 729874  
JH WV

REFERENCE DRAWING  
SEE PRODUCTION DRAWING 371584

CTDL HIGH SPEED 3-WAY "AND" PNP ALL LOADS



SEQUENCE OF OPERATION

1. ALL INPUTS DOWN, TRANSISTOR IS ON, THE OUTPUT IS UP
2. ANY ONE INPUT UP, THE TRANSISTOR IS OFF, THE OUTPUT IS DOWN
3. ALL INPUTS ON EXTENDER CARD MUST BE IN COINCIDENCE WITH INPUTS ON CARD FOR UP OUTPUT
4. PIN P IS CURRENT MODE OUTPUT
5. LOGIC BLOCKS MAY HAVE SYMBOLS OTHER THAN SHOWN

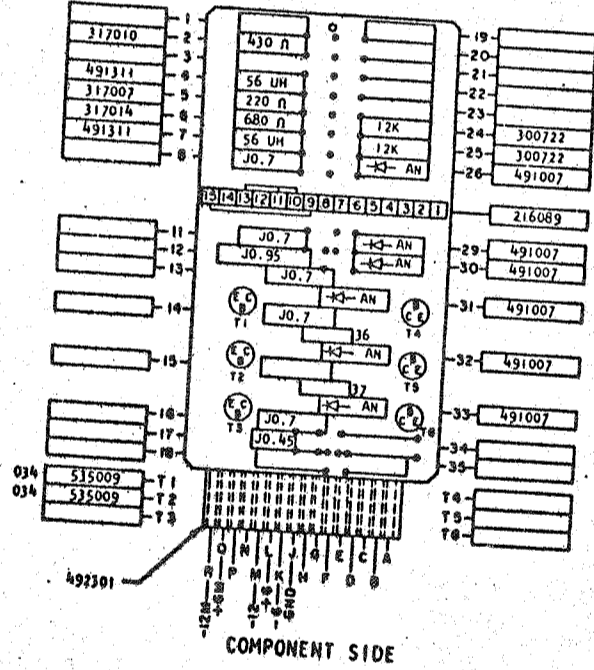
PINS	SIGNAL NAME	WAVE SHAPE	LEVELS	
			MIN	MAX
D, E, F	T INPUT	[Waveform]	UP	1.44 6.24
H	U OUTPUT	[Waveform]	DOWN	-7.44 -6.24
P	P OUTPUT	[Waveform]	UP	-5.44 .24
A, B, C	T INPUT	[Waveform]	DOWN	-7.44 -12.48
			UP	-4.93 -3.54
N	U OUTPUT	[Waveform]	DOWN	-8.82 -12.48
			UP	1.44 6.24
			DOWN	-7.44 -6.24
G	EXTENDER INPUT	[Waveform]	UP	-5.44 .24
			DOWN	-7.44 -12.48
			UP	+6
			DOWN	0.0

DELAY

	MINIMUM	MAXIMUM
TURN ON	0.15	0.50
TURN OFF	0.05	0.80

\*THIS DELAY CAN OCCUR ONLY ON HEAVILY LOADED BLOCKS.

NOTE: THE ABOVE RANGES OF DELAYS ARE REPRESENTATIVE. SPECIFIC CIRCUIT APPLICATION AND/OR WIRING CAPACITANCE MAY RESULT IN DELAYS WHICH ARE OUT OF THE GIVEN RANGES. IN SUCH CASES, CARD REPLACEMENT SHOULD INDICATE IF THE CIRCUIT IS OUT OF SPECIFICATIONS. EXAMPLE: LOGIC BLOCK DRIVING EP "OR".



COMPONENT SIDE

CIRCUIT AND PACKAGING STANDARD	
APPROVAL	DATE
ABC	4-2-62

INTERNATIONAL BUSINESS MACHINES CORP.				CIRCUIT AND PACKAGING STANDARD			
NAME	CARD	DATE	CHARGE NO.	APPROVAL	DATE	CHARGE NO.	APPROVAL
SPEED 3-WAY "AND" PNP ALL LOADS	ASM TSTR-CTDL HIGH	1/9 62	EC 115599				
DESIGN	BY	3-1-62	SCALE	NONE			
CHECK	BY	3-1-62	DRAW	LIG B-17-62			
APPRO			CHECK				

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