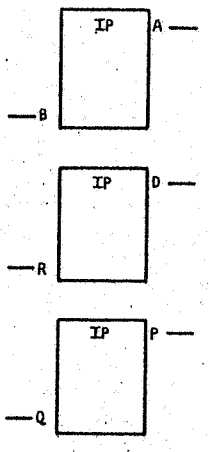
STANDARDS CODE 729910

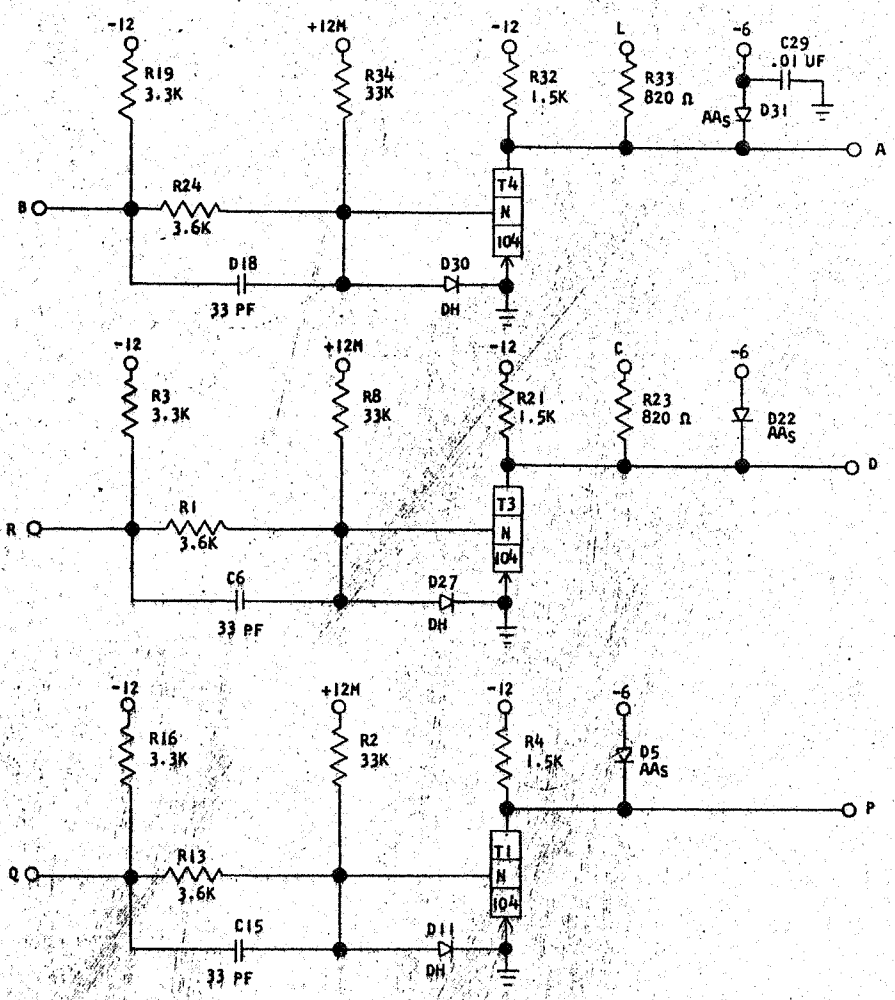
CARD CODE 72991C
D F Q -

REFERENCE DRAWING

SEE PRODUCTION DRAWING 370225

SDTDL INVERTING POWER DRIVER





SEQUENCE OF OPERATION

1. INPUT DOWN, TRANSISTOR ON, OUTPUT UP
2. INPUT UP, TRANSISTOR OFF, OUTPUT DOWN
3. 820Ω COLLECTOR RESISTOR RETURNED TO -12 VOLTS WHEN DRIVING NEGATIVE "OR" INPUTS OF DOUBLE LEVEL LOGIC BLOCKS & WHEN DRIVING TRIGGER AC INPUTS

PINS	SIGNAL NAME		WAVE SHAPE		LEVELS MIN	LEVELS MAX
B,R, Q	Y	INPUT		UP	-0.65	-0.10
				DOWN	-7.14	-5.84
A,D, P	Y	OUTPUT		UP	-0.65	-0.10
				DOWN	-6.06	-6.8

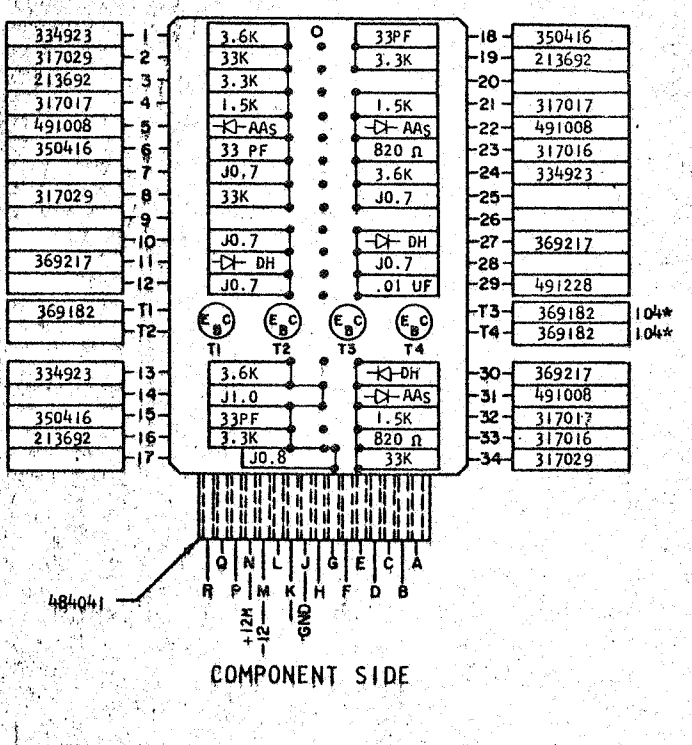
DELAY - NSEC

	MINIMUM	MAXIMUM
TURN ON	10.0**	50.0*
TURN OFF	14.0**	35.0*

*ASSUMES LOAD OF 10 LOGIC BLOCKS AND TR INPUT OF 70 NSEC AND INPUT TF OF 135 NSEC.
**ASSUMES LOAD OF 4 LOGIC BLOCKS AND INPUT TR OF 35 NSEC AND INPUT TF OF 70 NSEC.

RISE TIME	16.0	70.0# TO 110.0##
FALL TIME	75.0	125.0## TO 190.0#

#OCCURS WHEN DRIVING TRIGGERS
##OCCURS WHEN DRIVING LOGIC BLOCKS



COMPONENT SIDE

CIRCUIT AND PACKAGING STANDARD	
APPROVAL	DATE
ABC	4-2-62

INTERNATIONAL BUSINESS MACHINES CORP.					DATE	CHANGE NO.	APPROVAL	DATE	CHANGE NO.	APPROVAL	DEVELOPMENT NO.
NAME	CARD ASM TSTR-SDTDL INVERTING POWER DRIVER				6-29-62	115599					
DESIGN			MODEL	SMS							
DETAIL	RQ	3-1-62	SCALE	NONE							
CHECK	WH	3-1-62	DRAW	LIG 3-17-62							
APPRO			CHECK								

729910

C