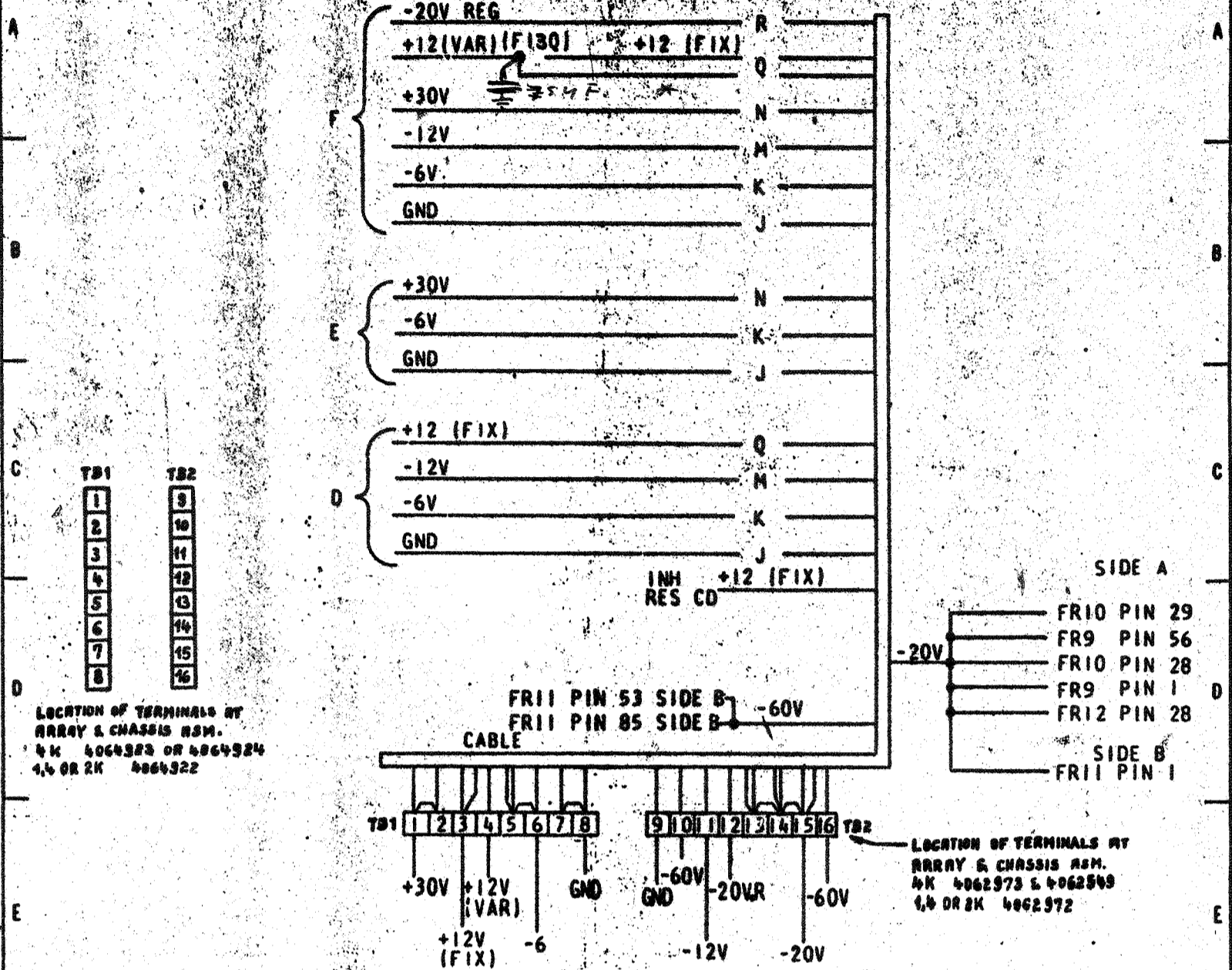
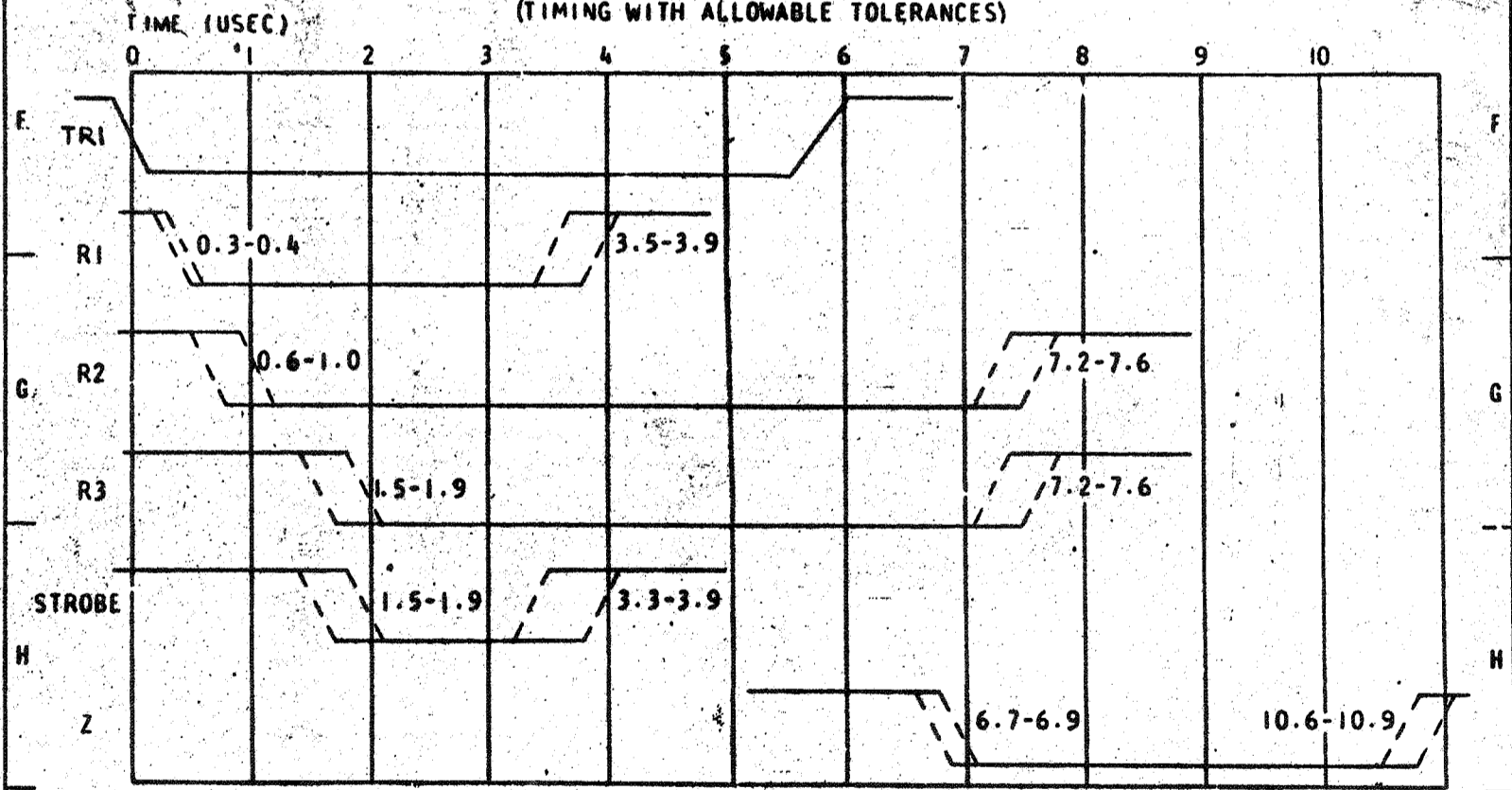


VOLTAGE DISTRIBUTION-BASIC MEMORY

* BM 486016



1401 CORE STORAGE CLOCK PULSES
(TIMING WITH ALLOWABLE TOLERANCES)



NOTE 1 DELAYS ARE MEASURED AT THE 50% LEVELS

DATE	EC NO	DATE	EC NO	DATE	EC NO
A 11-23-60	110404				
B 1-26-61	110423				
C 3-30-61	110432F				
	112912				

1429
722595