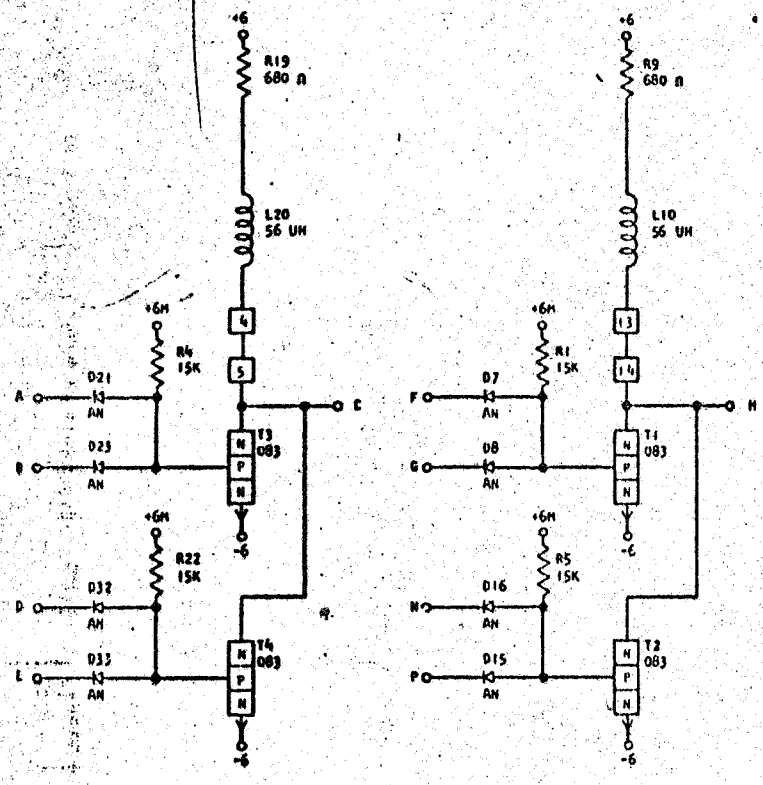
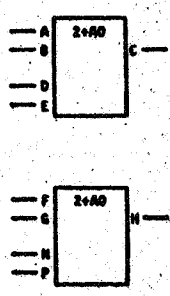


729898
STANDARD CODE

CARD CODE 729898
2J MX

REFERENCE DRAWING
SEE PRODUCTION DRAWING 370144

CTDL NPN TWO WAY GATE W/ COLLECTOR LOAD



SEQUENCE OF OPERATION

1. BOTH INPUTS TO A TRANSISTOR UP, TRANSISTOR ON OUTPUT DOWN
2. FOR UP OUTPUT, EITHER INPUT TO BOTH TRANSISTORS MUST BE DOWN
3. LOGIC BLOCKS MAY HAVE SYMBOLS OTHER THAN SHOWN

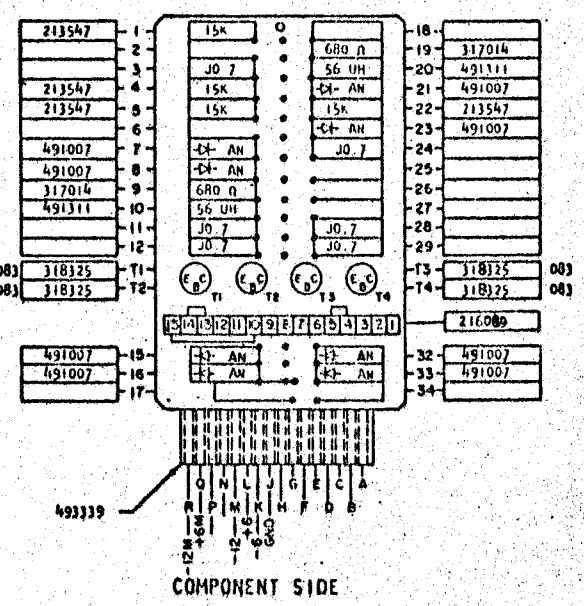
PINS	SIGNAL NAME	WAVE SHAPE	LEVELS	
			MIN	MAX
A, D, F, H	U INPUT		UP	-5.26 0.24
B, C, E, P	U INPUT		DOWN	-7.44 -12.5
C, N	T OUTPUT		UP	1.44 6.24
			DOWN	-5.46 -6.24

DELAY - USEC

	MINIMUM	MAXIMUM
TURN ON	0.05	0.70
TURN OFF	0.05	1.50

*THIS DELAY CAN OCCUR ONLY ON HEAVILY LOADED BLOCKS.

NOTE: THE ABOVE RANGES OF DELAYS ARE REPRESENTATIVE. SPECIFIC CIRCUIT APPLICATION AND/OR WIRING CAPACITANCE MAY RESULT IN DELAYS WHICH ARE OUT OF THE GIVEN RANGES. IN SUCH CASES, CARD REPLACEMENT SHOULD INDICATE IF THE CIRCUIT IS OUT OF SPECIFICATIONS. EXAMPLE: LOGIC BLOCK DRIVING OF "OR".



CIRCUIT AND PACKAGING STANDARD	
APPROVAL	DATE
ABC	4-2-62

INTERNATIONAL BUSINESS MACHINES CORP.				DATE	CHANGE NO.	APPROVAL	DATE	CHANGE NO.	APPROVAL	DEVELOPMENT NO.
NAME	CARD ASM	ISTR-CTDL	NPN	1-25-62	SC 115599					729898
NAME	TWO WAY GATE	W/ COLLECTOR LOAD		30-4-63	JJ B3687					
DESIGN		MODEL	SMS							
DETAIL	RQ	3-1-62	SCALE	NONE						
CHECK	WH	3-1-62	DRAWN	LEG	3-17-62					
APPROV			CHECK							

729898