729828

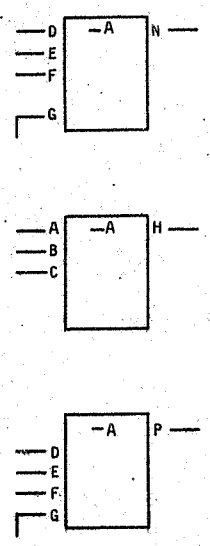
STANDARDS CODE

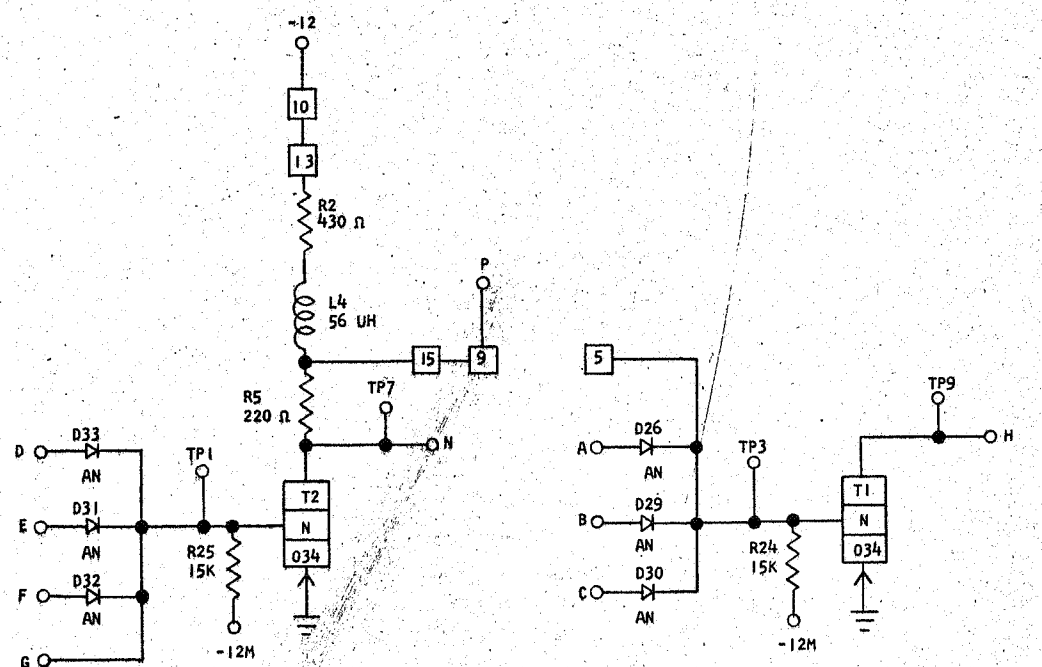
CARD CODE	729828
CJ VU	

REFERENCE DRAWING

SEE PRODUCTION DRAWING 371267

CTDL 3 WAY "AND" PNP ONE LOAD





SEQUENCE OF OPERATION

1. ALL INPUTS DOWN TRANSISTOR ON OUTPUT UP
2. ANY INPUT UP TRANSISTOR OFF OUTPUT DOWN
3. INPUTS ON EXTENDER CARD DOWN IN COINCIDENCE WITH DOWN INPUTS ON CARD FOR UP OUTPUT
4. T1 COLLECTOR MUST BE LOADED
5. LOGIC BLOCKS MAY HAVE SYMBOLS OTHER THAN SHOWN

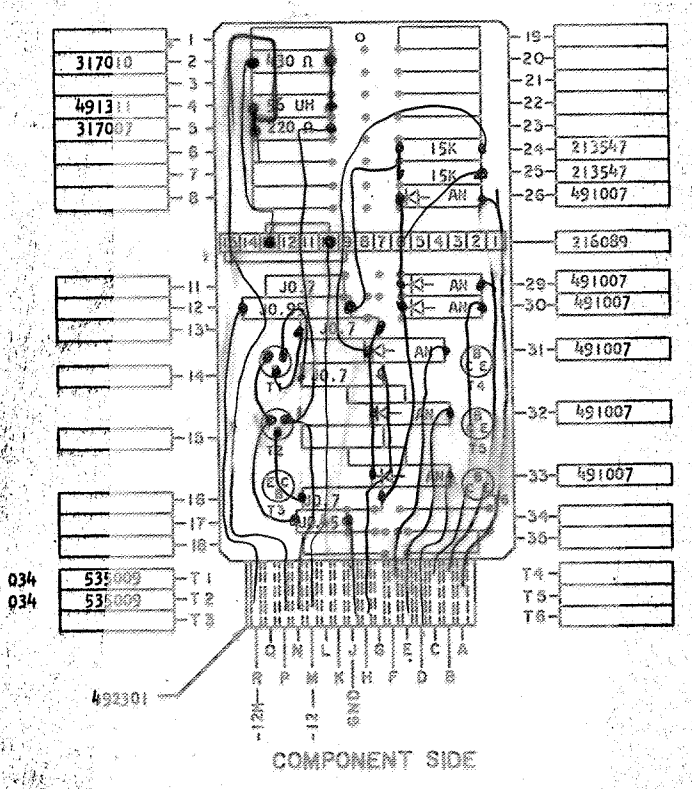
PINS		SIGNAL NAME	WAVE SHAPE	LEVELS	MIN	MAX
D, A	T	INPUT		UP	1.44	6.24
				DOWN	-0.74	-6.24
E, B	T	INPUT		UP	1.44	6.24
				DOWN	-0.74	-6.24
F, C	T	INPUT		UP	1.44	6.24
				DOWN	-0.74	-6.24
G		EXTENDER INPUT		UP	+6.0	
				DOWN	0.0	
H, N	U	OUTPUT		UP	-0.54	0.24
				DOWN	-7.44	-12.5
P	P	OUTPUT		UP	-4.93	-3.54
				DOWN	-8.82	-12.5

DELAY - USEC

	MINIMUM	MAXIMUM
TURN ON	0.10	0.80
TURN OFF	0.05	0.80*

*THIS DELAY CAN OCCUR ONLY ON HEAVILY LOADED BLOCK.

NOTE: THE ABOVE RANGES OF DELAYS ARE REPRESENTATIVE. SPECIFIC CIRCUIT APPLICATION AND/OR WIRING CAPACITANCE MAY RESULT IN DELAYS WHICH ARE OUT OF THE GIVEN RANGES. IN SUCH CASES, CARD REPLACEMENT SHOULD INDICATE IF THE CIRCUIT IS OUT OF SPECIFICATIONS. EXAMPLE: LOGIC BLOCK DRIVING EF "OR".



CIRCUIT AND PACKAGING STANDARD	
APPROVAL	DATE
ABC	4-2-62

INTERNATIONAL BUSINESS MACHINES CORP.						DATE	CHANGE NO.	APPROVAL	DATE	CHANGE NO.	APPROVAL	DEVELOPMENT NO.
NAME	CARD ASM TSTR - CTDL - 3 WAY "AND" PNP ONE LOAD					6-21-62	115599					729828
DESIGN			MODEL	SMS								
DETAIL	RQ	3-1-62	SCALE	NONE								
CHECK	WH	3-1-62	DRAW	LIG	3-17-62							
APPRO			CHECK									

C

729828