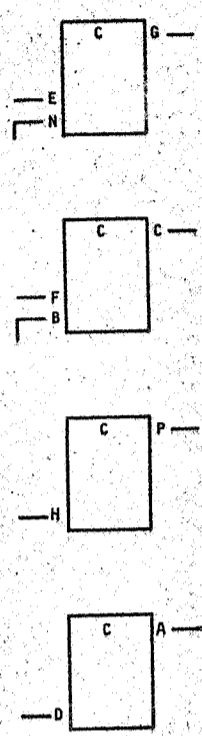
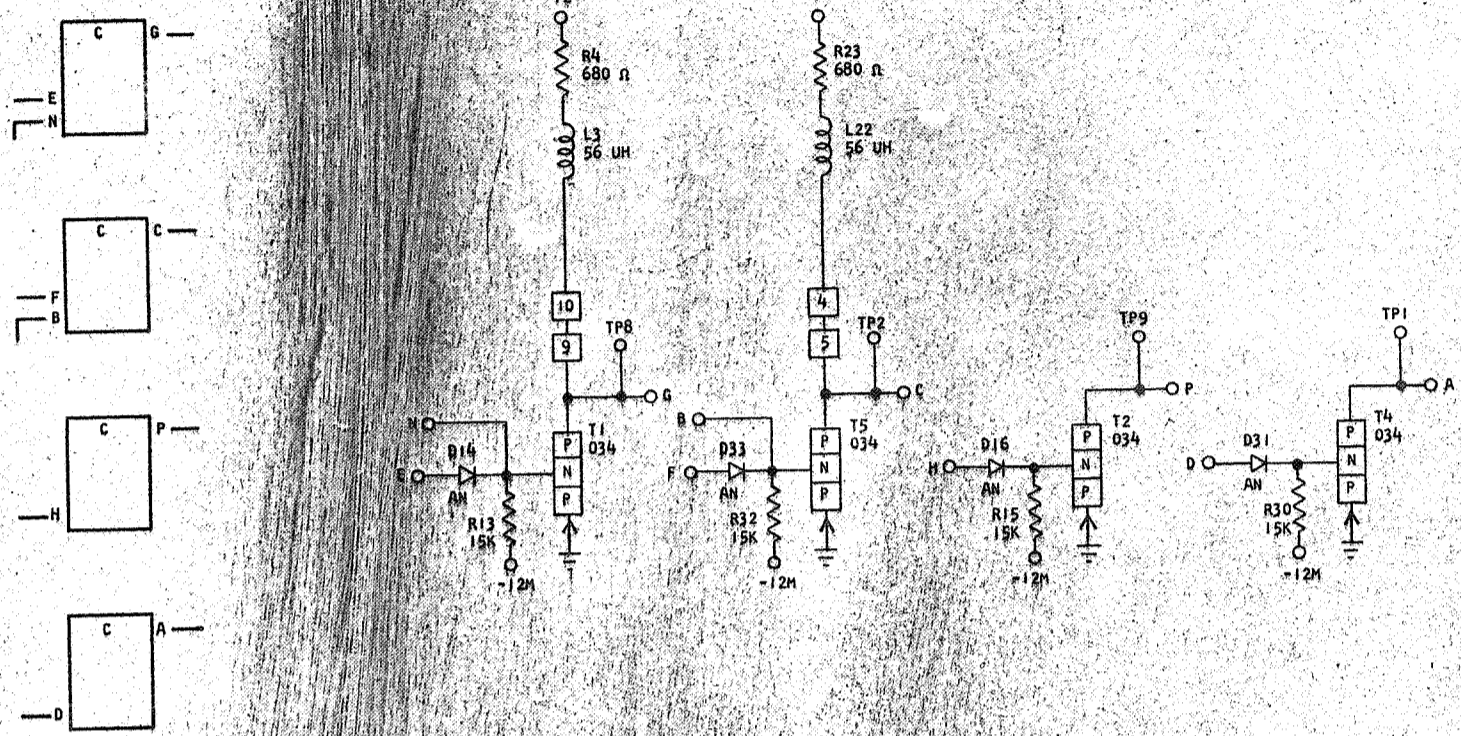


STANDARDS CODE

CARD CODE 729847
CQ ZT

REFERENCE DRAWING
SEE PRODUCTION DRAWING 371272

CTDL - ONE WAY PNP 2 LOADS
FOUR ON CARD



SEQUENCE OF OPERATION

1. INPUT DOWN TRANSISTOR ON OUTPUT UP
2. INPUT UP TRANSISTOR OFF OUTPUT DOWN
3. INPUTS ON EXTENDER DOWN IN COINCIDENCE WITH DOWN INPUT ON CARD FOR UP OUTPUT
4. T2, T4 COLLECTORS MUST BE LOADED
5. LOGIC BLOCKS MAY HAVE SYMBOLS OTHER THAN SHOWN

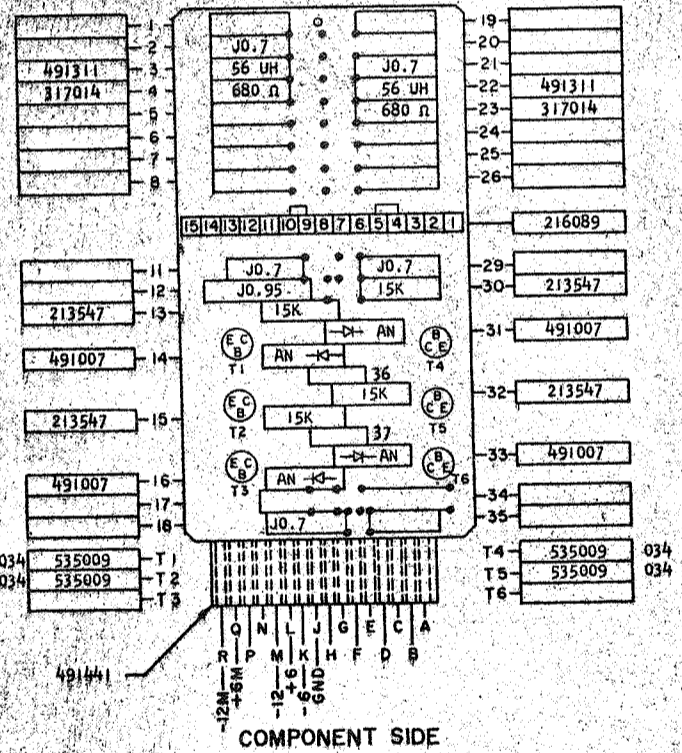
PINS	SIGNAL NAME	WAVE SHAPE	LEVELS	
			MIN	MAX
E, F, H, D	T INPUT	[Waveform]	UP	1.44 6.24
			DOWN	-0.74 -6.24
N, B	EXTENDER INPUT	[Waveform]	UP	+6.0
			DOWN	0.0
G, C, P, A	U OUTPUT	[Waveform]	UP	-0.54 0.24
			DOWN	-7.44 -12.5

DELAY - USEC

	MINIMUM	MAXIMUM
TURN ON	0.10	0.80
TURN OFF	0.05	0.80*

*THIS DELAY CAN OCCUR ONLY ON HEAVILY LOADED BLOCKS.

NOTE: THE ABOVE RANGES OF DELAYS ARE REPRESENTATIVE. SPECIFIC CIRCUIT APPLICATION AND/OR WIRING CAPACITANCE MAY RESULT IN DELAYS WHICH ARE OUT OF THE GIVEN RANGES. IN SUCH CASES, CARD REPLACEMENT SHOULD INDICATE IF THE CIRCUIT IS OUT OF SPECIFICATIONS. EXAMPLE: LOGIC BLOCK DRIVING EF "OR".



CIRCUIT AND PACKAGING STANDARD	
APPROVAL	DATE
ABC	4-2-62

INTERNATIONAL BUSINESS MACHINES CORP.				DATE	CHANGE NO.	APPROVAL	DATE	CHANGE NO.	APPROVAL	DEVELOPMENT NO.
NAME CARD ASM TSTR-CTDL-ONE				6-27-62	115599					729847
WAY PNP 2 LDS-FOUR ON CARD										
DESIGN	RQ	3-1-62	SCALE	SMS						
DETAIL	LN	3-1-62	SCALE	NONE						
CHECK	LN	3-1-62	DRAW	LIG	3-17-62					
APPRO			CHECK							

729847