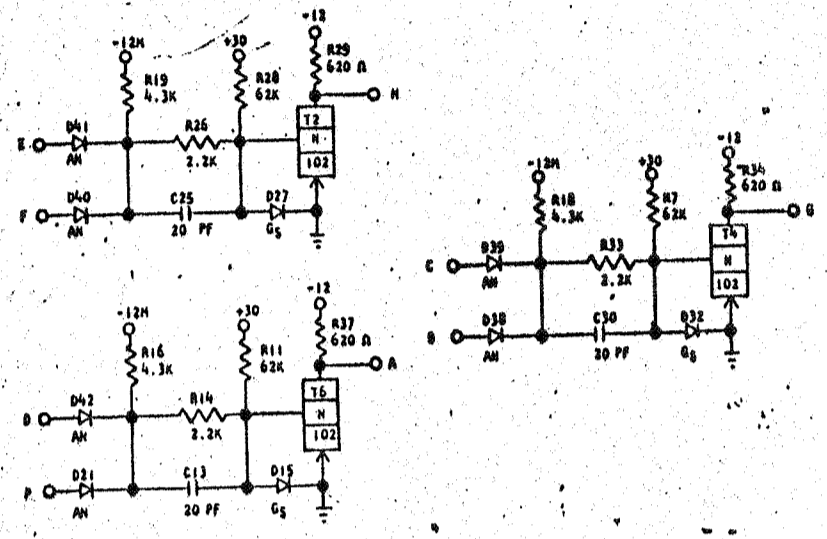
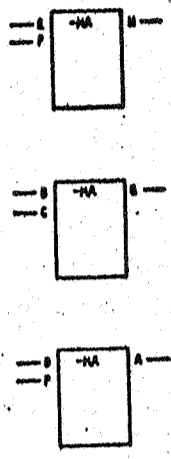


729855
STANDARD CODE

6480 0004 729855
D A B -

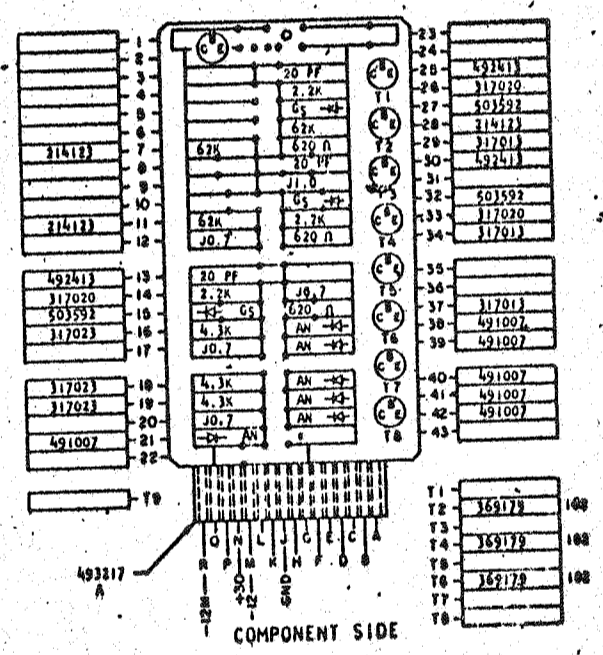
REFERENCE DRAWING
SEE PRODUCTION DRAWING 37192A

DTDL - AND GATE



- SEQUENCE OF OPERATION**
1. INPUTS DOWN TRANSISTOR ON OUTPUT UP
 2. EITHER OR BOTH INPUTS UP TRANSISTOR OFF AND OUTPUT IS DOWN
 3. DRIVING CTDL LOADS:
MAXIMUM TURN ON DELAY - .18 USEC
MAXIMUM TURN OFF DELAY - .09 USEC
- DRIVING WORST CASE CLOCK LOAD
MAXIMUM TURN ON DELAY - .15 USEC
MAXIMUM TURN OFF DELAY - .144 USEC

PINS	SIGNAL NAME	WAVE SHAPES	LEVELS	
			MIN	MAX
1, 2, 3, 4, 5, 6	T INPUTS		UP	+1.44 +6.24
7, 8, 9	M OUTPUTS		DOWN	-4.46 -6.24
10, 11, 12	T INPUTS		UP	+1.09 +2.4
13, 14, 15	A OUTPUTS		DOWN	-7.44 -12.48



CIRCUIT AND PACKAGING STANDARDS	
APPROVAL	DATE
ABC	4-2-62

INTERNATIONAL BUSINESS MACHINES CORP.				DATE	CHANGE NO.	APPROVAL	DATE	CHANGE NO.	APPROVAL	DEVELOPMENT NO.
NAME	CARD ASM TEST - DTDL -			6-19-62	115599					
	AND GATE			1-3-63	20116034					
DESIGN		MODIFIED	SMS	30.4.63	7783687					
DETAIL	RD 1-1-62	SCALE	NONE							
CHECK	WH 1-1-62	DRAW	LTC 1-17-62							
APPROV		CHECK								

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