

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
101			JOB		FORTRAN COMPILER -- SENSE LIGHT PHASE -- PHASE 43								
102			CTL		6611								
103			*										
104			*		IN-LINE INSTRUCTIONS ARE GENERATED.								
105			*										
106			X1	EQU	89			0089					
107			X2	EQU	94			0094					
108			X3	EQU	99			0099					
109			*										
110			*		STUFF IN THE RESIDENT AREA								
111			*										
112			GLOBER	EQU	184 GLOBAL ERROR FLAG -- WM MEANS ERROR			0184					
113			*										
114				EXT00	SNAPSH, LOADNX, CDOVLY					MACRO			
115			SNAPSH	EQU	333			0333		GEN			
116			PHASLD	EQU	381			0381		GEN			
117			SNAPEX	EQU	564			0564		GEN			
118			LOADNX	EQU	700 CARD OVERLAY UNLESS NOP			0700		GEN			
119			CDOVLY	EQU	700 1 IF LOADING FROM CARDS, N IF FROM TAPE			0700		GEN			
120			TPREAD	EQU	704 LOAD OVERLAY FROM TAPE			0704		GEN			
121			TPERR	EQU	728			0728		GEN			
122			*										
123				EXT03	START, TOP OF PHASE 3					MACRO			
124			BEGIN3	EQU	838			0838		GEN			
125			TOP3	EQU	2600			2600		GEN			
126			*										
127			PHAS43	LDPH	LIGHT,LOADAD,BEGN43,,,43					MACRO			
			*	PHAZ	LDPH [PHASID],LOADAD,ENTAD[,SKIPFG,SKIP],[NUMBER][,HALT]					GEN			
			*	XFR	PHASZ PROHIBITED IN A MACRO					GEN			
			*							GEN			
			*	LOAD	A BLOCK					GEN			
			*							GEN			
128)6J003	EQU	110 PHASE ID			0110		GEN			
129)6K003	EQU	700 LOAD NEXT PHASE			0700		GEN			
130)6L003	EQU	704 TAPE READ INSTRUCTION			0704		GEN			
131)6M003	EQU	728 TAPE ERROR HANDLER			0728		GEN			
			*							GEN			
132				ORG	201				0201				
133			PHAS43	BSS)8J003,G		5	0201	B 257 G	GEN	1	257	
134				NOF	TO PATCH IN TRAPS FOR DEBUGGING		1	0206	N	GEN	1		
135)0J003	EQU	*&1			0207		GEN			
136				LCA)9J003,)6J003		7	0207	L 277 110	GEN	1	277	110
137				BCE)1J003,)6K003,1 Q: LOADING FROM CARDS?		8	0214	B 250 700 1	GEN	1	250	700
138				BCE)1J003,)6L003&4,0 Q: LOADING FROM AUTOCODER TAPE?		8	0222	B 250 708 0	GEN	1	250	708
139				RTW	1,LOADAD READ THE BLOCK		8	0230	L %U1 838 R	GEN	1	%U1	838
140				BER)6M003 Q: TAPE ERROR?		5	0238	B 728 L	GEN	2	728	
141				CS	BEGN43,)9R003 ENTER THE BLOCK		7	0243	/ 838 281	GEN	2	838	281
142)1J003	CS)6K003,)9R003 LOAD CARDS OR AUTOCODER TAPE		7	0250	/ 700 281	GEN	2	700	281
143)8J003	SW)9R003		4	0257	, 281	GEN	2	281	
144				MU	%T0,)8K003,W		8	0261	M %T0 273 W	GEN	2	%T0	273

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145				H)0J003	4		0269	. 207	GEN	2	207	
146)8K003	EQU	*&1			0273		GEN			
147)9J003	DCW	@LIGHT@ PHASE ID	5		0277		GEN	3		
148				DCW	#1	1		0278		GEN	3		
149				DC	@43@ PHASE NUMBER	2		0280		GEN	3		
150)9R003	DCW	@}@	1		0281		GEN	3		
151				XFR	PHAS43				B 201		4	201	
152			*										
153				ORG	BEGIN3				0838				
154			LOADAD	EQU	*&1 LOAD ADDRESS			0838					
155	838		BEGN43	BCE	DONE,0&X1,	8		0838	B 861 0 0		5	861	000+1
156	846			MCW	0&X1,SEQCOD	7		0846	M 0 0 /69		5	000+1	1169
157	853			BCE	SLITE,SEQCOD-3,J	8		0853	B 865 /66 J		5	865	1166
158	861		DONE	B	LOADNX	4		0861	B 700		5	700	
159	884		SLITE	LCA	0&X1,0&X3	7		0865	L 0 0 0?0		5	000+1	000+3
160	891			SAR	X1	4		0872	Q 089		5	089	
161	895			C	0&X3	4		0876	C 0?0		6	000+3	
162	899			SAR	X3	4		0880	Q 099		6	099	
163	903			SBR	TSTBRK&6,1&X1	7		0884	H /07 0 1		6	1107	001+1
164	910			MCW	RBRACK,1&X1	7		0891	M /70 0 1		6	1170	001+1
165	917			LCA	1&X3,2&X3	7		0898	L 0?1 0?2		6	001+3	002+3
166	924			SBR	X3	4		0905	H 099		6	099	
167	928			MCW	SEQCOD,W3	7		0909	M /69 /73		7	1169	1173
168	935			BWZ	*&5,W3,2	8		0916	V 928 /73 2		7	928	1173
169	943			B	*&9	4		0924	B 936		7	936	
170	947			BWZ	*&15,W3-2,2	8		0928	V 950 /71 2		7	950	1171
171	955			MCW	W3,X2	7		0936	M /73 094		7	1173	094
172	962			MCW	0&X2,W3	7		0943	M 0!0 /73		8	000+2	1173
173	969			BCE	SYNTAX,0&X1,}	8		0950	B 996 0 0 } GMARK		8	996	000+1
174	977			MCW	0&X1,W2	7		0958	M 0 0 /75		8	000+1	1175
175	984			BCE	TSTCOD,W2-1,} SENSE LIGHT NUMBER IS ONE DIGIT?	8		0965	B 977 /74 } GMARK		8	977	1174
176	992			B	SYNTAX	4		0973	B 996		8	996	
177	996		TSTCOD	MN	0&X1,*&8	7		0977	D 0 0 991		9	000+1	991
178	1 003			BCE	SENSOK,K01234,0 VALID SENSE LIGHT NUMBER?	8		0984	B 49 /80 0		9	1049	1180
179	1 011			CHAIN	4					MACRO			
180				BCE		1		0992	B	GEN	9		
181				BCE		1		0993	B	GEN	9		
182				BCE		1		0994	B	GEN	9		
183				BCE		1		0995	B	GEN	9		
184	1 015		SYNTAX	CS	332	4		0996	/ 332		9	332	
185	1 019			CS		1		1000	/		10		
186	1 020			SW	GLOBER	4		1001	, 184		10	184	
187	1 024			MN	W3,245	7		1005	D /73 245		10	1173	245
188	1 031			MN		1		1012	D		10		
189	1 032			MN		1		1013	D		10		
190	1 033			MCW	ERR36	4		1014	M S22		10	1222	
191	1 037			W		1		1018	2		10		
192	1 038			BCV	*&5	5		1019	B 28 @		11	1028	
193	1 043			B	*&3	4		1024	B 30		11	1030	
194	1 047			CC	1	2		1028	F 1		11		

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
195	1	049		SBR	X3,4&X3	7		1030	H 099 0?4		11	099	004+3
196	1	056		C	0&X1	4		1037	C 0 0		11	000+1	
197	1	060		SAR	X1	4		1041	Q 089		11	089	
198	1	064		B	BEGN43	4		1045	B 838		11	838	
199	1	068	SENSOK	MZ	*-4,0&X1	7		1049	Y 51 0 0		12	1051	000+1
200	1	075		BCE	SENSE0,0&X1,0	8		1056	B /43 0 0 0		12	1143	000+1
201	1	083		MN	0&X1,CW&3	7		1064	D 0 0 /65		12	000+1	1165
202	1	090		LCA	CW&3,0&X3 LOAD CW INSTRUCTION	7		1071	L /65 0?0		12	1165	000+3
203	1	097		SBR	X3	4		1078	H 099		12	099	
204	1	101	ENDSTM	C	0&X1	4		1082	C 0 0		12	000+1	
205	1	105		SAR	X1	4		1086	Q 089		13	089	
206	1	109		LCA	1&X1,0&X3 GMWM	7		1090	L 0 1 0?0		13	001+1	000+3
207	1	116		SBR	X3	4		1097	H 099		13	099	
208	1	120	TSTBRK	BCE	BEGN43,0,] NOT TOO BIG IF BRACKET NOT CLOBBBERED	8		1101	B 838 000]		13	838	000
209	1	128		CS	332	4		1109	/ 332		13	332	
210	1	132		CS		1		1113	/		13		
211	1	133		CC	1	2		1114	F 1		13		
212	1	135		MCW	ERROR2,270	7		1116	M S58 270		14	1258	270
213	1	142		W		1		1123	2		14		
214	1	143		CC	1	2		1124	F 1		14		
215	1	145		BCE	HALT,CDOVLY,1	8		1126	B /39 700 1		14	1139	700
216	1	153		RWD	1	5		1134	U %U1 R		14	%U1	
217	1	158	HALT	H	HALT	4		1139	. /39		14	1139	
218	1	162	SENSE0	LCA	SW,0&X3 CHAINED SW	7		1143	L S59 0?0		14	1259	000+3
219	1	169		LCA	SW2&6 SW 82,84	4		1150	L S66		15	1266	
220	1	173		SBR	X3	4		1154	H 099		15	099	
221	1	177		B	ENDSTM	4		1158	B 82		15	1082	
222	1	181	CW	CW	80	4		1162) 080		15	080	
223	1	188	SEQCOD	DCW	#4	4		1169			15		
224	1	195	RBRACK	DCW	@]@	1		1170			15		
225	1	198	W3	DCW	#3	3		1173			15		
226	1	200	W2	DCW	#2	2		1175			16		
227	1	205	K01234	DCW	@01234@	5		1180			16		
228	1	247	ERR36	DCW	@ERROR 36 - ILLEGAL SENSE LIGHT, STATEMENT @	42		1222			18		
229	1	283	ERROR2	DCW	@MESSAGE 2 - OBJECT PROGRAM TOO LARGE@	36		1258			18		
230	1	284	SW	SW		1		1259	,		19		
231	1	285	SW2	SW	82,84	7		1260	, 082 084		19	082	084
232	1	292	GMWM	DCW	@)@	1		1267		GMARK	19		
233			XFR		BEGN43				B 838		20	838	
234			CLRME	CLRA	LOADAD,GMWM,C					MACRO			
			*	CLRA	CLRBOT,CLRTOP[,SS,HERE,GWMAD]					GEN			
			*							GEN			
			*	CLEAR CORE	AFTER A PHASE USING THE CLRTOP ADDRESS					GEN			
			*							GEN			
235			ORG		201				0201				
			*							GEN			
			*	CLEAR DOWN	TO CLRBOT & X00 THE EASY WAY					GEN			
			*							GEN			
236			CLRME	EQU	*&1			0201		GEN			
237			BSS		SNAPSH,C	5		0201	B 333 C	GEN	21	333	

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
238)0J004	CS	GMWM CLEAR FROM CLRTOP	4		0206	/ S67	GEN	21	1267	
239				SBR)0J004&3	4		0210	H 209	GEN	21	209	
240				SBR)0L004&6	4		0214	H 255	GEN	21	255	
241				C)0J004&3,)0M004 DOWN TO CLRBOT & X00?	7		0218	C 209 266	GEN	21	209	266
242				BU)0J004	5		0225	B 206 /	GEN	21	206	
			*							GEN			
			*		NOW CLEAR DOWN TO CLRBOT THE HARD WAY					GEN			
			*							GEN			
243)0K004	C)0L004&6,)0N004	7		0230	C 255 269	GEN	21	255	269
244				BU)0L004	5		0237	B 249 /	GEN	22	249	
245				CS	LOADNX,)0Q004 LOAD THE NEXT BLOCK AT 1	7		0242	/ 700 276	GEN	22	700	276
246)0L004	LCA)0P004,0-0 CLEAR WITH BLANK AND WORD MARK	7		0249	L 270 000	GEN	22	270	000
247				SBR)0L004&6	4		0256	H 255	GEN	22	255	
248				B)0K004	4		0260	B 230	GEN	22	230	
249)0M004	DSA)0R004 CLRBOT & X00 - 1	3		0266	899	GEN	22	899	
250)0N004	DSA	LOADAD CLRBOT	3		0269	838	GEN	22	838	
251)0P004	DCW	#1	1		0270		GEN	23		
252				DC	@CLRA @ IDENTIFY IN A DECK, TAPE, OR DUMP	5		0275		GEN	23		
253)0Q004	DCW	@}@	1		0276		GEN	23		
254				ORG	LOADAD&X00				0900				
255)0R004	EQU	* CLRBOT & X00 - 1			0899		GEN			
256				XFR	CLRME				B 201		24	201	

SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS
)0J003	0207: 0)0J004	0206: 0)0K004	0230: 0)0L004	0249: 0)0M004	0266: 0)0N004	0269: 0
)0P004	0270: 0)0Q004	0276: 0)0R004	0899: 0)1J003	0250: 0)6J003	0110: 0)6K003	0700: 0
)6L003	0704: 0)6M003	0728: 0)8J003	0257: 0)8K003	0273: 0)9J003	0277: 0)9R003	0281: 0
BEGIN3	0838: 0	BEGN43	0838: 0	CDOVLY	0700: 0	CLRME	0201: 0	CW	1162: 0	DONE	0861: 0
ENDSTM	1082: 0	ERR36	1222: 0	ERROR2	1258: 0	GLOBER	0184: 0	GMWM	1267: 0	HALT	1139: 0
K01234	1180: 0	LOADAD	0838: 0	LOADNX	0700: 0	PHAS43	0201: 0	PHASLD	0381: 0	RBRACK	1170: 0
SENSE0	1143: 0	SENSOK	1049: 0	SEQCOD	1169: 0	SLITE	0865: 0	SNAPEX	0564: 0	SNAPSH	0333: 0
SW	1259: 0	SW2	1260: 0	SYNTAX	0996: 0	TOP3	2600: 0	TPERR	0728: 0	TPREAD	0704: 0
TSTBRK	1101: 0	TSTCOD	0977: 0	W2	1175: 0	W3	1173: 0	X1	0089: 0	X2	0094: 0
X3	0099: 0										

UNREFERENCED SYMBOLS

PHASLD SNAPEX TOP3 TPERR TPREAD