

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
101			JOB		FORTRAN COMPILER -- DO PHASE -- PHASE 46								
102			CTL		6611								
103			*										
104			*		STRINGS OF UNCONDITIONAL BRANCH INSTRUCTIONS AND PARAMETERS								
105			*		ARE GENERATED IN-LINE. AN UNCONDITIONAL BRANCH IS GENERATED								
106			*		TO FOLLOW THE LAST STATEMENT WITHIN THE RANGE OF THE DO								
107			*										
108			X1	EQU	89			0089					
109			X2	EQU	94			0094					
110			X3	EQU	99			0099					
111			*										
112			*		STUFF IN THE RESIDENT AREA								
113			*										
114			NEGAR2	EQU	142 LOOKS LIKE NEGARY -- SEE PHASE 20			0142					
115			DOCNT	EQU	151 COUNT OF DO STATEMENTS			0151					
116			GLOBER	EQU	184 GLOBAL ERROR FLAG -- WM MEANS ERROR			0184					
117			*										
118				EXT00	SNAPSH, LOADNX, CDOVLY					MACRO			
119			SNAPSH	EQU	333			0333		GEN			
120			PHASLD	EQU	381			0381		GEN			
121			SNAPEX	EQU	564			0564		GEN			
122			LOADNX	EQU	700 CARD OVERLAY UNLESS NOP			0700		GEN			
123			CDOVLY	EQU	700 1 IF LOADING FROM CARDS, N IF FROM TAPE			0700		GEN			
124			TPREAD	EQU	704 LOAD OVERLAY FROM TAPE			0704		GEN			
125			TPERR	EQU	728			0728		GEN			
126			*										
127			TABEL	EQU	2499 IN RESORT THREE PHASE 3 ???			2499					
128			*										
129				EXT03	START, TOP OF PHASE 3					MACRO			
130			BEGIN3	EQU	838			0838		GEN			
131			TOP3	EQU	2600			2600		GEN			
132			*										
133			PHAS46	LDPH	DOMSK,LOADAD,BEGN46,,,46					MACRO			
			*	PHAZ	LDPH [PHASID],LOADAD,ENTAD[,SKIPFG,SKIP],[NUMBER][,HALT]					GEN			
			*	XFR	PHASZ PROHIBITED IN A MACRO					GEN			
			*							GEN			
			*	LOAD	A BLOCK					GEN			
			*							GEN			
134)6J003	EQU	110 PHASE ID			0110		GEN			
135)6K003	EQU	700 LOAD NEXT PHASE			0700		GEN			
136)6L003	EQU	704 TAPE READ INSTRUCTION			0704		GEN			
137)6M003	EQU	728 TAPE ERROR HANDLER			0728		GEN			
			*							GEN			
138				ORG	201				0201				
139			PHAS46	BSS)8J003,G		5	0201	B 257 G	GEN	1	257	
140				NOP	TO PATCH IN TRAPS FOR DEBUGGING		1	0206	N	GEN	1		
141)0J003	EQU	*&1			0207		GEN			
142				LCA)9J003,)6J003		7	0207	L 277 110	GEN	1	277	110
143				BCE)1J003,)6K003,1 Q: LOADING FROM CARDS?		8	0214	B 250 700 1	GEN	1	250	700
144				BCE)1J003,)6L003&4,0 Q: LOADING FROM AUTOCODER TAPE?		8	0222	B 250 708 0	GEN	1	250	708

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
145				RTW	1,LOADAD			8	0230	L %U1 838 R	GEN	1	%U1 838
146				BER)6M003			5	0238	B 728 L	GEN	2	728
147				CS	BEGN46,)9R003			7	0243	/ 838 281	GEN	2	838 281
148)1J003	CS)6K003,)9R003			7	0250	/ 700 281	GEN	2	700 281
149)8J003	SW)9R003			4	0257	, 281	GEN	2	281
150				MU	%T0,)8K003,W			8	0261	M %T0 273 W	GEN	2	%T0 273
151				H)0J003			4	0269	. 207	GEN	2	207
152)8K003	EQU	*&1				0273		GEN		
153)9J003	DCW	@DOMSK@			5	0277		GEN	3	
154				DCW	#1			1	0278		GEN	3	
155				DC	@46@			2	0280		GEN	3	
156)9R003	DCW	@}@			1	0281		GEN	3	
157				XFR	PHAS46					B 201	GEN	4	201
158				*									
159				*	DO PHASE ALGORITHM								
160				*									
161				*	OUTER								
162				*									
163				*	NO SAME DIFF								
164				*	I B XT B XT B XT								
165				*	N								
166				*	N GM T BK T IN T BK								
167				*	E EZ B BK B IN B BK								
168				*	R HD - BK - IN - BK								
169				*									
170				ORG	BEGIN3				0838				
171			LOADAD	EQU	*&1				0838				
172	838		BEGN46	SW	GM,GM3			7	0838	, V72 W00	GEN	5	1572 1600
173	845			SW	GM4,GM2			7	0845	, W05 V96	GEN	5	1605 1596
174	852			MCW	X3,SX3			7	0852	M 099 W11	GEN	5	099 1611
175	859		LOOP	BW	DONE,0&X1			8	0859	V V33 0 0 1	GEN	5	1533 000+1
176	867			MCW	KLESS,2&X1			7	0867	M W12 0 2	GEN	5	1612 002+1
177	874			SBR	TSTLES&6,2&X1			7	0874	H S65 0 2	GEN	6	1265 002+1
178	881			C	0&X1			4	0881	C 0 0	GEN	6	000+1
179	885			SAR	X1			4	0885	Q 089	GEN	6	089
180	889			C	2&X1,KD DO STATEMENT?			7	0889	C 0 2 W13	GEN	6	002+1 1613
181	896			BU	ALMOST NO			5	0896	B V26 /	GEN	6	1526
182	901			CW	111,112			7	0901) 111 112	GEN	6	111 112
183	908			CW	113,114			7	0908) 113 114	GEN	7	113 114
184	915			MCW	5&X1,X2 ADDRESS OF SEQUENCE NUMBER			7	0915	M 0 5 094	GEN	7	005+1 094
185	922			MCW	0&X2,SEQNO			7	0922	M 0!0 W16	GEN	7	000+2 1616
186	929			MCW	0&X1,X2			7	0929	M 0 0 094	GEN	7	000+1 094
187	936			SAR	X1			4	0936	Q 089	GEN	7	089
188	940			MCW	0&X2,SEQEND			7	0940	M 0!0 W19	GEN	7	000+2 1619
189	947			ZA	SEQNO,SEQDIF			7	0947	? W16 W22	GEN	8	1616 1622
190	954			S	SEQEND,SEQDIF			7	0954	S W19 W22	GEN	8	1619 1622
191	961			MCW	NOP,SWICH1			7	0961	M W23 S12	GEN	8	1623 1212
192	968			BWZ	MSG38,SEQDIF,B ILLEGAL RANGE IF POSITIVE			8	0968	V T97 W22 B	GEN	8	1397 1622
193	976			MCW	X1,X2			7	0976	M 089 094	GEN	8	089 094
194	983			MCW	KB3,F5			7	0983	M W26 V95	GEN	9	1626 1595

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
195		990		MCW	KT, LONGOP	7		0990	M W27 W01		9	1627	1601
196		997		MCW	BRANCH, SWICH2	7		0997	M W28 S16		9	1628	1216
197	1	004	NESTED	C	0&X2 DOWN TO BODY OF STMT BELOW DO	4		1004	C 0!0		9	000+2	
198	1	008		C		1		1008	C		9		
199	1	009		SAR	X2	4		1009	Q 094		9	094	
200	1	013		C	2&X2, KD IS IT A DO STATEMENT?	7		1013	C 0!2 W13		9	002+2	1613
201	1	020		BU	NOTDO	5		1020	B 90 /		10	1090	
202	1	025		MCW	0&X2, X3	7		1025	M 0!0 099		10	000+2	099
203	1	032		C	0&X3, SEQNO PROPERLY NESTED?	7		1032	C 0?0 W16		10	000+3	1616
204	1	039		BH	NESTED YES	5		1039	B 04 U		10	1004	
205	1	044		C	0&X3, SEQEND	7		1044	C 0?0 W19		10	000+3	1619
206	1	051		BH	MSG39 ILLEGAL DO NESTING	5		1051	B U42 U		10	1442	
207	1	056		BCE	*&8, 1&X2, H CO-ENDING?	8		1056	B 71 0!1 H		11	1071	001+2
208	1	064		MCW	KE, 1&X2 NOT CO-ENDING	7		1064	M W29 0!1		11	1629	001+2
209	1	071		BL	NOTDO	5		1071	B 90 T		11	1090	
210	1	076		MCW	KH, 1&X2 CO-ENDING AFTER ALL	7		1076	M W30 0!1		11	1630	001+2
211	1	083		MCW	5&X2, F5	7		1083	M 0!5 V95		11	005+2	1595
212	1	090	NOTDO	BCE	COEND, 4&X1, H	8		1090	B /20 0 4 H		12	1120	004+1
213	1	098		MCW	NOP, SWICH2	7		1098	M W23 S16		12	1623	1216
214	1	105		BCE	*&8, 4&X1, }	8		1105	B /20 0 4 } GMARK		12	1120	004+1
215	1	113		MCW	BRANCH, LONGOP	7		1113	M W28 W01		12	1628	1601
216	1	120	COEND	MCW	SEQEND, LONG	7		1120	M W19 W08		12	1619	1608
217	1	127		SW	6&X1	4		1127	, 0 6		13	006+1	
218	1	131		MCW	8&X1, SHORT	7		1131	M 0 8 V99		13	008+1	1599
219	1	138		MCW	8&X1, F6	7		1138	M 0 8 W04		13	008+1	1604
220				*									
221				*	TEST SYNTAX AND GENERATE CODE								
222				*									
223	1	145	GEN	B	SUB	4		1145	B T01		13	1301	
224	1	149		DCW	@, @	1		1149			13		
225	1	152		DSA	F4	3		1152	V92		13	1592	
226	1	153		B	SUB	4		1153	B T01		13	1301	
227	1	157		DCW	@#@	1		1157			14		
228	1	160		DSA	F1	3		1160	V83		14	1583	
229	1	161		B	SUB	4		1161	B T01		14	1301	
230	1	165		DCW	@, @	1		1165			14		
231	1	168		DSA	F2	3		1168	V86		14	1586	
232	1	169		BW	NRBOT, 0&X1	8		1169	V T86 0 0 1		14	1386	000+1
233	1	177		B	SUB	4		1177	B T01		14	1301	
234	1	181		DCW	@, @	1		1181			15		
235	1	184		DSA	F3	3		1184	V89		15	1589	
236	1	185		BW	BOTTOM, 0&X1	8		1185	V /97 0 0 1		15	1197	000+1
237	1	193		B	MSG40 SYNTAX ERROR	4		1193	B U80		15	1480	
238				*									
239	1	197	BOTTOM	MCW	SX3, X3	7		1197	M W11 099		15	1611	099
240	1	204		MN	0&X1	4		1204	D 0 0		15	000+1	
241	1	208		SAR	X1	4		1208	Q 089		15	089	
242	1	212	SWICH1	NOP	TSTLES	4		1212	N S59		16	1259	
243	1	216	SWICH2	NOP	SKIP	4		1216	N S40		16	1240	
244	1	220		A	KP1, DOCNT	7		1220	A W31 151		16	1631	151

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
245	1	227		LCA	LONG,0&X3	7		1227	L W08 0?0		16	1608	000+3
246	1	234		LCA		1		1234	L		16		
247	1	235		LCA		1		1235	L		16		
248	1	236		SBR	X3	4		1236	H 099		16	099	
249	1	240	SKIP	LCA	SHORT,0&X3	7		1240	L V99 0?0		17	1599	000+3
250	1	247		CHAIN	8					MACRO			
251				LCA		1		1247	L	GEN	17		
252				LCA		1		1248	L	GEN	17		
253				LCA		1		1249	L	GEN	17		
254				LCA		1		1250	L	GEN	17		
255				LCA		1		1251	L	GEN	17		
256				LCA		1		1252	L	GEN	17		
257				LCA		1		1253	L	GEN	18		
258				LCA		1		1254	L	GEN	18		
259	1	255		SBR	SX3	4		1255	H W11		18	1611	
260	1	259	TSTLES	BCE	LOOP,0,<	8		1259	B 859 000 <		18	859	000
261	1	267		CS	332	4		1267	/ 332		18	332	
262	1	271		CS		1		1271	/		18		
263	1	272		CC	1	2		1272	F 1		18		
264	1	274		MCW	ERROR2,270	7		1274	M W67 270		19	1667	270
265	1	281		W		1		1281	2		19		
266	1	282		CC	1	2		1282	F 1		19		
267	1	284		BCE	HALT,CDOVLY,1	8		1284	B S97 700 1		19	1297	700
268	1	292		RWD	1	5		1292	U %U1 R		19	%U1	
269	1	297	HALT	H	HALT	4		1297	. S97		19	1297	
270				*									
271				*	CHECK THE NEXT CHARACTER AGAINST THE ONE AT 0&X1, THEN								
272				*	CHECK THAT THE NEXT THREE HAVE NUMERIC PARTS IN 0..9,								
273				*	THAT IS, THEY CONSTITUTE AN ADDRESS.								
274				*									
275	1	301	SUB	SBR	X2	4		1301	H 094		19	094	
276	1	305		C	0&X1,0&X2	7		1305	C 0 0 0!0		20	000+1	000+2
277	1	312		SAR	X1	4		1312	Q 089		20	089	
278	1	316		BU	MSG40 SYNTAX ERROR IF NOT THE EXPECTED CHAR	5		1316	B U80 /		20	1480	
279	1	321		MCW	3&X2,*&7	7		1321	M 0!3 T34		20	003+2	1334
280	1	328		MCW	0&X1,0	7		1328	M 0 0 000		20	000+1	000
281	1	335		S	W1	4		1335	S W68		20	1668	
282	1	339	GOTDIG	A	KP1,W1	7		1339	A W31 W68		21	1631	1668
283	1	346		BCE	4&X2,W1,D EXIT IF THREE TIMES THROUGH LOOP	8		1346	B 0!4 W68 D		21	004+2	1668
284	1	354		MN	0&X1,*&12	7		1354	D 0 0 T72		21	000+1	1372
285	1	361		SAR	X1	4		1361	Q 089		21	089	
286	1	365		BCE	GOTDIG,DIGITS,0	8		1365	B T39 W78 0		21	1339	1678
287	1	373		CHAIN	9					MACRO			
288				BCE		1		1373	B	GEN	21		
289				BCE		1		1374	B	GEN	21		
290				BCE		1		1375	B	GEN	22		
291				BCE		1		1376	B	GEN	22		
292				BCE		1		1377	B	GEN	22		
293				BCE		1		1378	B	GEN	22		
294				BCE		1		1379	B	GEN	22		

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
295				BCE		1		1380	B	GEN	22		
296				BCE		1		1381	B	GEN	22		
297	1	382		B	MSG40 SPECIAL CHARACTER MEANS SYNTAX ERROR	4		1382	B U80		23	1480	
298				*									
299	1	386	NRBOT	MCW	NEGAR2,F3	7		1386	M 142 V89		23	142	1589
300	1	393		B	BOTTOM	4		1393	B /97		23	1197	
301				*									
302				*	ILLEGAL RANGE OF DO								
303				*									
304	1	397	MSG38	CS	332	4		1397	/ 332		23	332	
305	1	401		CS		1		1401	/		23		
306	1	402		SW	GLOBER	4		1402	, 184		23	184	
307	1	406		MN	SEQNO,245	7		1406	D W16 245		23	1616	245
308	1	413		MN		1		1413	D		24		
309	1	414		MN		1		1414	D		24		
310	1	415		MCW	ERR38	4		1415	M X20		24	1720	
311	1	419		W		1		1419	2		24		
312	1	420		BCV	*&5	5		1420	B U29 @		24	1429	
313	1	425		B	*&3	4		1425	B U31		24	1431	
314	1	429		CC	1	2		1429	F 1		24		
315	1	431	SET1	MCW	BRANCH,SWICH1	7		1431	M W28 S12		25	1628	1212
316	1	438		B	GEN	4		1438	B /45		25	1145	
317				*									
318				*	ILLEGAL NESTING								
319				*									
320	1	442	MSG39	CS	332	4		1442	/ 332		25	332	
321	1	446		CS		1		1446	/		25		
322	1	447		SW	GLOBER	4		1447	, 184		25	184	
323	1	451		MN	SEQNO,241	7		1451	D W16 241		25	1616	241
324	1	458		MN		1		1458	D		25		
325	1	459		MN		1		1459	D		26		
326	1	460		MCW	ERR39	4		1460	M X58		26	1758	
327	1	464		W		1		1464	2		26		
328	1	465		BCV	*&5	5		1465	B U74 @		26	1474	
329	1	470		B	*&3	4		1470	B U76		26	1476	
330	1	474		CC	1	2		1474	F 1		26		
331	1	476		B	SET1	4		1476	B U31		26	1431	
332				*									
333				*	SYNTAX ERROR								
334				*									
335	1	480	MSG40	CS	332	4		1480	/ 332		27	332	
336	1	484		CS		1		1484	/		27		
337	1	485		SW	GLOBER	4		1485	, 184		27	184	
338	1	489		MN	SEQNO,235	7		1489	D W16 235		27	1616	235
339	1	496		MN		1		1496	D		27		
340	1	497		MN		1		1497	D		27		
341	1	498		MCW	ERR40	4		1498	M X90		27	1790	
342	1	502		W		1		1502	2		28		
343	1	503		BCV	*&5	5		1503	B V12 @		28	1512	
344	1	508		B	*&3	4		1508	B V14		28	1514	

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
345	1	512		CC	1	2		1512	F 1		28		
346	1	514		C	1&X1	4		1514	C 0 1		28	001+1	
347	1	518		SAR	X1	4		1518	Q 089		28	089	
348	1	522		B	TSTLES	4		1522	B S59		28	1259	
349				*									
350	1	526	ALMOST	SBR	X1,5&X1	7		1526	H 089 0 5		29	089	005+1
351	1	533	DONE	MCW	SX3,X3	7		1533	M W11 099		29	1611	099
352	1	540		MN	0&X3	4		1540	D 0?0		29	000+3	
353	1	544		SAR	X2	4		1544	Q 094		29	094	
354	1	548	CSLOOP	CS	0&X2	4		1548	/ 0!0		29	000+2	
355	1	552		SBR	X2	4		1552	H 094		29	094	
356	1	556		C	0&X2,DOEND	7		1556	C 0!0 X99		29	000+2	1799
357	1	563		BU	CSLOOP	5		1563	B V48 /		30	1548	
358	1	594		B	LOADNX	4		1568	B 700		30	700	
359	1	598	GM	DC	@}@	1		1572		GMARK	30		
360				*									
361				*	GENERATED CODE TEMPLATE								
362				*									
363	1	602		DCW	@T924@	4		1576			30		
364	1	606		DCW	@T921@	4		1580			30		
365	1	609	F1	DCW	#3	3		1583			30		
366	1	612	F2	DCW	#3	3		1586			30		
367	1	615	F3	DCW	#3	3		1589			30		
368	1	618	F4	DCW	#3	3		1592			31		
369	1	621	F5	DCW	#3	3		1595			31		
370	1	622	GM2	DC	@}@	1		1596		GMARK	31		
371	1	625	SHORT	DC	#3	3		1599			31		
372	1	626	GM3	DC	@}@	1		1600		GMARK	31		
373	1	627	LONGOP	DCW	@T@	1		1601			31		
374	1	630	F6	DC	#3	3		1604			31		
375	1	631	GM4	DC	@}@	1		1605		GMARK	31		
376	1	634	LONG	DC	#3	3		1608			31		
377				*									
378				*	DATA								
379				*									
380	1	637	SX3	DCW	#3	3		1611			31		
381	1	638	KLESS	DCW	@<@	1		1612			31		
382	1	639	KD	DCW	@D@	1		1613			31		
383	1	642	SEQNO	DCW	#3 SEQUENCE NUMBER OF DO	3		1616			31		
384	1	645	SEQEND	DCW	#3 SEQUENCE NUMBER OF FINAL STATEMENT OF DO	3		1619			32		
385	1	648	SEQDIF	DCW	#3 SEQNO - SEQEND -- BETTER BE NEGATIVE	3		1622			32		
386	1	649	NOP	NOP		1		1623	N		32		
387	1	652	KB3	DCW	#3	3		1626			32		
388	1	653	KT	DCW	@T@	1		1627			32		
389	1	654	BRANCH	B		1		1628	B		32		
390	1	655	KE	DCW	@E@	1		1629			32		
391	1	656	KH	DCW	@H@	1		1630			33		
392	1	657	KP1	DCW	&1	1		1631			33		
393	1	693	ERROR2	DCW	@MESSAGE 2 - OBJECT PROGRAM TOO LARGE@	36		1667			33		
394	1	694	W1	DCW	#1	1		1668			33		

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
395	1	704	DIGITS	DCW	@0123456789@	10		1678			34		
396	1	746	ERR38	DCW	@ERROR 38 - ILLEGAL RANGE OF DO, STATEMENT @	42		1720			36		
397	1	784	ERR39	DCW	@ERROR 39 - ILLEGAL NESTING, STATEMENT @	38		1758			37		
398	1	816	ERR40	DCW	@ERROR 40 - DO SYNTAX, STATEMENT @	32		1790			38		
399				ORG	*X00				1800				
400			DOEND	EQU	*			1799					
401	1	825		DCW	@}@	1		1800		GMARK	39		
402				XFR	BEGN46				B 838		40	838	
403			CLRME	CLRA	LOADAD,TABEL,E					MACRO			
			*	CLRA	CLRBOT,CLRTOP[,SS,HERE,GWMAD]					GEN			
			*							GEN			
			*	CLEAR CORE	AFTER A PHASE USING THE CLRTOP ADDRESS					GEN			
			*							GEN			
404				ORG	201				0201				
			*							GEN			
			*	CLEAR DOWN	TO CLRBOT & X00 THE EASY WAY					GEN			
			*							GEN			
405			CLRME	EQU	*&1			0201		GEN			
406				BSS	SNAPSH,E	5		0201	B 333 E	GEN	41	333	
407)0J004	CS	TABEL CLEAR FROM CLRTOP	4		0206	/ M99	GEN	41	2499	
408				SBR)0J004&3	4		0210	H 209	GEN	41	209	
409				SBR)0L004&6	4		0214	H 255	GEN	41	255	
410				C)0J004&3,)0M004 DOWN TO CLRBOT & X00?	7		0218	C 209 266	GEN	41	209	266
411				BU)0J004	5		0225	B 206 /	GEN	41	206	
			*							GEN			
			*	NOW CLEAR	DOWN TO CLRBOT THE HARD WAY					GEN			
			*							GEN			
412)0K004	C)0L004&6,)0N004	7		0230	C 255 269	GEN	41	255	269
413				BU)0L004	5		0237	B 249 /	GEN	42	249	
414				CS	LOADNX,)0Q004 LOAD THE NEXT BLOCK AT 1	7		0242	/ 700 276	GEN	42	700	276
415)0L004	LCA)0P004,0-0 CLEAR WITH BLANK AND WORD MARK	7		0249	L 270 000	GEN	42	270	000
416				SBR)0L004&6	4		0256	H 255	GEN	42	255	
417				B)0K004	4		0260	B 230	GEN	42	230	
418)0M004	DSA)0R004 CLRBOT & X00 - 1	3		0266	899	GEN	42	899	
419)0N004	DSA	LOADAD CLRBOT	3		0269	838	GEN	42	838	
420)0P004	DCW	#1	1		0270		GEN	43		
421				DC	@CLRA @ IDENTIFY IN A DECK, TAPE, OR DUMP	5		0275		GEN	43		
422)0Q004	DCW	@}@	1		0276		GEN	43		
423				ORG	LOADAD&X00				0900				
424)0R004	EQU	* CLRBOT & X00 - 1			0899		GEN			
425				XFR	CLRME				B 201		44	201	

SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS
)0J003	0207: 0)0J004	0206: 0)0K004	0230: 0)0L004	0249: 0)0M004	0266: 0)0N004	0269: 0
)0P004	0270: 0)0Q004	0276: 0)0R004	0899: 0)1J003	0250: 0)6J003	0110: 0)6K003	0700: 0
)6L003	0704: 0)6M003	0728: 0)8J003	0257: 0)8K003	0273: 0)9J003	0277: 0)9R003	0281: 0
ALMOST	1526: 0	BEGIN3	0838: 0	BEGN46	0838: 0	BOTTOM	1197: 0	BRANCH	1628: 0	CDOVLY	0700: 0
CLRME	0201: 0	COEND	1120: 0	CSLOOP	1548: 0	DIGITS	1678: 0	DOCNT	0151: 0	DOEND	1799: 0
DONE	1533: 0	ERR38	1720: 0	ERR39	1758: 0	ERR40	1790: 0	ERROR2	1667: 0	F1	1583: 0
F2	1586: 0	F3	1589: 0	F4	1592: 0	F5	1595: 0	F6	1604: 0	GEN	1145: 0
GLOBER	0184: 0	GM	1572: 0	GM2	1596: 0	GM3	1600: 0	GM4	1605: 0	GOTDIG	1339: 0
HALT	1297: 0	KB3	1626: 0	KD	1613: 0	KE	1629: 0	KH	1630: 0	KLESS	1612: 0
KP1	1631: 0	KT	1627: 0	LOADAD	0838: 0	LOADNX	0700: 0	LONG	1608: 0	LONGOP	1601: 0
LOOP	0859: 0	MSG38	1397: 0	MSG39	1442: 0	MSG40	1480: 0	NEGAR2	0142: 0	NESTED	1004: 0
NOF	1623: 0	NOTDO	1090: 0	NRBOT	1386: 0	PHAS46	0201: 0	PHASLD	0381: 0	SEQDIF	1622: 0
SEQEND	1619: 0	SEQNO	1616: 0	SET1	1431: 0	SHORT	1599: 0	SKIP	1240: 0	SNAPEX	0564: 0
SNAPSH	0333: 0	SUB	1301: 0	SWICH1	1212: 0	SWICH2	1216: 0	SX3	1611: 0	TABEL	2499: 0
TOP3	2600: 0	TPERR	0728: 0	TPREAD	0704: 0	TSTLES	1259: 0	W1	1668: 0	X1	0089: 0
X2	0094: 0	X3	0099: 0								

UNREFERENCED SYMBOLS

PHASLD SNAPEX TOP3 TPERR TPREAD