

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
101				JOB	FORTRAN COMPILER -- SNAPSHOT -- PHASE 53S								
102				CTL	6611								
103				*									
104				*	SAME AS SNAPSHOT IN PHASE 00, EXCEPT THERE'S A GMMW AT THE END								
105				*	OF EXECUTED IN THE LAST DATA OBJECT.								
106				*									
107			X1	EQU	89			0089					
108			X2	EQU	94			0094					
109			X3	EQU	99			0099					
110				*									
111				*	STUFF IN THE RESIDENT AREA								
112				*									
113			PHASID	EQU	110			0110					
114			TOPCOR	EQU	688 TOP CORE ADDRESS FROM PARAM CARD			0688					
115				*									
116				EXT00	SNAPSH, LOADNX, CDOVLY					MACRO			
117			SNAPSH	EQU	333			0333		GEN			
118			PHASLD	EQU	381			0381		GEN			
119			SNAPEX	EQU	564			0564		GEN			
120			LOADNX	EQU	700 CARD OVERLAY UNLESS NOP			0700		GEN			
121			CDOVLY	EQU	700 1 IF LOADING FROM CARDS, N IF FROM TAPE			0700		GEN			
122			TPREAD	EQU	704 LOAD OVERLAY FROM TAPE			0704		GEN			
123			TPERR	EQU	728			0728		GEN			
124				*									
125				XT52B	STUFF IN PHASE 52BC					MACRO			
126			BEG52C	EQU	934 RELOCATION TAG FROM SW INSTRUCTION			0934		GEN			
127			CLR52C	EQU	1696			1696		GEN			
128				*									
129				*	ALL WE DO HERE IS CLEAR 52C BECAUSE THE RELOCATABLE LIBRARY COMES								
130				*	IMMEDIATELY AFTER IT, THEN RELOAD THE SNAPSHOT PHASE, THEN LOAD								
131				*	THE NEXT PHASE.								
132				*									
133			CLR52	CLRA	BEG52C, CLR52C					MACRO			
				*	CLRA CLRBOT, CLRTOP [, SS, HERE, GWMAD]					GEN			
				*						GEN			
				*	CLEAR CORE AFTER A PHASE USING THE CLRTOP ADDRESS					GEN			
				*						GEN			
134				ORG	201			0201					
				*						GEN			
				*	CLEAR DOWN TO CLRBOT & X00 THE EASY WAY					GEN			
				*						GEN			
135			CLR52	EQU	*&1			0201		GEN			
136			)0J003	CS	CLR52C CLEAR FROM CLRTOP	4		0201 / W96		GEN	1	1696	
137			SBR	)0J003&3		4		0205 H 204		GEN	1	204	
138			SBR	)0L003&6		4		0209 H 250		GEN	1	250	
139			C	)0J003&3, )0M003	DOWN TO CLRBOT & X00?	7		0213 C 204 261		GEN	1	204	261
140			BU	)0J003		5		0220 B 201 /		GEN	1	201	
				*						GEN			
				*	NOW CLEAR DOWN TO CLRBOT THE HARD WAY					GEN			
				*						GEN			

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
141			)0K003	C	)0L003&6,)0N003	7		0225	C 250 264	GEN	1	250	264
142				BU	)0L003	5		0232	B 244 /	GEN	1	244	
143				CS	LOADNX,)0Q003 LOAD THE NEXT BLOCK AT 1	7		0237	/ 700 271	GEN	2	700	271
144			)0L003	LCA	)0P003,0-0 CLEAR WITH BLANK AND WORD MARK	7		0244	L 265 000	GEN	2	265	000
145				SBR	)0L003&6	4		0251	H 250	GEN	2	250	
146				B	)0K003	4		0255	B 225	GEN	2	225	
147			)0M003	DSA	)0R003 CLRBOT & X00 - 1	3		0261	999	GEN	2	999	
148			)0N003	DSA	BEG52C CLRBOT	3		0264	934	GEN	2	934	
149			)0P003	DCW	#1	1		0265		GEN	2		
150				DC	@CLRA @ IDENTIFY IN A DECK, TAPE, OR DUMP	5		0270		GEN	2		
151			)0Q003	DCW	@}@	1		0271		GEN	3		
152				ORG	BEG52C&X00				1000				
153			)0R003	EQU	* CLRBOT & X00 - 1			0999		GEN			
154				XFR	CLR52				B 201		4	201	
155				*									
156			PHS53S	LDPH	SNAPSHOT,SNAPSH,LOADNX,,53S					MACRO			
			*	PHAZ	LDPH [PHASID],LOADAD,ENTAD[,SKIPFG,SKIP],[NUMBER][,HALT]					GEN			
			*	XFR	PHASZ PROHIBITED IN A MACRO					GEN			
			*							GEN			
			*	LOAD	A BLOCK					GEN			
			*							GEN			
157			)6J004	EQU	110 PHASE ID			0110		GEN			
158			)6K004	EQU	700 LOAD NEXT PHASE			0700		GEN			
159			)6L004	EQU	704 TAPE READ INSTRUCTION			0704		GEN			
160			)6M004	EQU	728 TAPE ERROR HANDLER			0728		GEN			
			*							GEN			
161				ORG	201				0201				
162			PHS53S	BSS	)8J004,G	5		0201	B 257 G	GEN	5	257	
163				NOP	TO PATCH IN TRAPS FOR DEBUGGING	1		0206	N	GEN	5		
164			)0J004	EQU	*&1			0207		GEN			
165				LCA	)9J004,)6J004	7		0207	L 280 110	GEN	5	280	110
166				BCE	)1J004,)6K004,1 Q: LOADING FROM CARDS?	8		0214	B 250 700 1	GEN	5	250	700
167				BCE	)1J004,)6L004&4,0 Q: LOADING FROM AUTOCODER TAPE?	8		0222	B 250 708 0	GEN	5	250	708
168				RTW	1,SNAPSH READ THE BLOCK	8		0230	L %U1 333 R	GEN	5	%U1	333
169				BER	)6M004 Q: TAPE ERROR?	5		0238	B 728 L	GEN	6	728	
170				CS	LOADNX,)9R004 ENTER THE BLOCK	7		0243	/ 700 285	GEN	6	700	285
171			)1J004	CS	)6K004,)9R004 LOAD CARDS OR AUTOCODER TAPE	7		0250	/ 700 285	GEN	6	700	285
172			)8J004	SW	)9R004	4		0257	, 285	GEN	6	285	
173				MU	%T0,)8K004,W	8		0261	M %T0 273 W	GEN	6	%T0	273
174				H	)0J004	4		0269	. 207	GEN	6	207	
175			)8K004	EQU	*&1			0273		GEN			
176			)9J004	DCW	@SNAPSHOT@ PHASE ID	8		0280		GEN	7		
177				DCW	#1	1		0281		GEN	7		
178				DC	@53S@ PHASE NUMBER	3		0284		GEN	7		
179			)9R004	DCW	@}@	1		0285		GEN	7		
180				XFR	PHS53S				B 201		8	201	
181			*										
182				ORG	333				0333				
183	333		MYSNAP	SBR	EXIT&3 SAME AS SNAPSH	4		0333	H 567		9	567	
184	337			SBR	SXX&6	4		0337	H 408		9	408	

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
185	341			MCW	KZ3,ADR5-2	7		0341	M 661 656		9	661	656
186	348			MCW	X3,SX3&6	7		0348	M 099 415		9	099	415
187	355			MCW	X1,SX1&6	7		0355	M 089 422		9	089	422
188	362			SBR	X1,1	7		0362	H 089 001		9	089	001
189	369			SBR	X3,202	7		0369	H 099 202		10	099	202
190	376			CS	332	4		0376	/ 332		10	332	
191	380			CS		1		0380	/		10		
192	381			MCW	PHASID,210	7		0381	M 110 210		10	110	210
193	388			BSS	SKIP,F	5		0388	B 621 F		10	621	
194				*									
195				*	PRINT A HEADER								
196				*									
197	393			CC	1	2		0393	F 1		10		
198	395			MCW	X2,250	7		0395	M 094 250		10	094	250
199	402	SXX		SBR	216,0	7		0402	H 216 000		11	216	000
200	409	SX3		SBR	256,0	7		0409	H 256 000		11	256	000
201	416	SX1		SBR	244,0	7		0416	H 244 000		11	244	000
202	423			W		1		0423	2		11		
203	424			CC	K	2		0424	F K		11		
204	426			ZA	KP2,W2A	7		0426	? 662 664		11	662	664
205	433	CLEARH		CS	332	4		0433	/ 332		11	332	
206	437			CS		1		0437	/		12		
207	438			CC	J	2		0438	F J		12		
208	440			MCW	ADR5,306	7		0440	M 658 306		12	658	306
209	447			MCW		1		0447	M		12		
210	448			SBR	LOOP&6	4		0448	H 465		12	465	
211	452			MCW	K9,W2B-1	7		0452	M 665 668		12	665	668
212	459	LOOP		MCW	W2B-1,000	7		0459	M 668 000		12	668	000
213	466			MCW	DOTS	4		0466	M 651		13	651	
214	470			SBR	LOOP&6	4		0470	H 465		13	465	
215	474			A	KM10,W2B	7		0474	A 667 669		13	667	669
216	481			BWZ	LOOP,W2B-1,2	8		0481	V 459 668 2		13	459	668
217	489			A	KP1,ADR5-2	7		0489	A 670 656		13	670	656
218	496			W		1		0496	2		13		
219	497	GET		SW	0&X3	4		0497	, 0?0		13	000+3	
220	501			MCW	0&X1,0&X3	7		0501	M 0 0 0?0		14	000+1	000+3
221	508			BW	DOWM,0&X1	8		0508	V 520 0 0 1		14	520	000+1
222	516			CW	0&X3	4		0516	) 0?0		14	000+3	
223	520	DOWM		C	X1,TOPCOR	7		0520	C 089 688		14	089	688
224	527			BU	CONT	5		0527	B 568 /		14	568	
225	532			W		1		0532	2		14		
226	533			WM		2		0533	2 )		14		
227	535	RX1		MCW	SX1&6,X1	7		0535	M 422 089		15	422	089
228	542			MCW	SX3&6,X3	7		0542	M 415 099		15	415	099
229	549			CS	332	4		0549	/ 332		15	332	
230	553			CS		1		0553	/		15		
231	554			BSS	HALT,G	5		0554	B 563 G		15	563	
232	559			B	EXIT	4		0559	B 564		15	564	
233	563	HALT		H		1		0563	.		15		
234	564	EXIT		B	0-0	4		0564	B 000		16	000	

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
235	568		CONT	SBR	X1,1&X1	7	0568	H 089 0	1		16	089	001+1
236	575			BCE	BUMP3,X3-2,2	8	0575	B 632 097	2		16	632	097
237	583			SBR	X3,201	7	0583	H 099 201			16	099	201
238	590			W		1	0590	2			16		
239	591			WM		2	0591	2 )			16		
240	593			A	KP1,W2A	7	0593	A 670 664			16	670	664
241	600			C	W2A,KP15	7	0600	C 664 672			17	664	672
242	607			BU	CLEARH	5	0607	B 433 /			17	433	
243	612			S	W2A	4	0612	S 664			17	664	
244	616			CCB	CLEARH,1	5	0616	F 433 1			17	433	
245	621	SKIP		MCW	XQTD,220	7	0621	M 680 220			17	680	220
246	628			W	RX1	4	0628	2 535			17	535	
247	632	BUMP3		A	KP1,X3	7	0632	A 670 099			17	670	099
248	639			B	GET	4	0639	B 497			18	497	
249	651	DOTS		DCW	@9.....@	9	0651				18		
250	653			DCW	@9-@	2	0653				18		
251	658	ADR5		DCW	00000 FIVE DIGIT ADDRESS	5	0658				18		
252	661	KZ3		DCW	000	3	0661				18		
253	662	KP2		DCW	&2	1	0662				18		
254	664	W2A		DCW	#2	2	0664				18		
255	665	K9		DCW	9	1	0665				19		
256	667	KM10		DCW	@I0@	2	0667				19		
257	669	W2B		DCW	#2	2	0669				19		
258	670	KP1		DCW	&1	1	0670				19		
259	672	KP15		DCW	&15	2	0672				19		
260	679			DCW	@EXECUTE@	7	0679				19		
261	*	680	XQTD	DCW	@}@ CHANGED TO D BY RELOADER PHASE 53R	1	0680			GMARK	19		
262				XFR	LOADNX JUST LOAD THE NEXT PHASE				B 700		20	700	

SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS
)0J003	0201: 0	)0J004	0207: 0	)0K003	0225: 0	)0L003	0244: 0	)0M003	0261: 0	)0N003	0264: 0
)0P003	0265: 0	)0Q003	0271: 0	)0R003	0999: 0	)1J004	0250: 0	)6J004	0110: 0	)6K004	0700: 0
)6L004	0704: 0	)6M004	0728: 0	)8J004	0257: 0	)8K004	0273: 0	)9J004	0280: 0	)9R004	0285: 0
ADR5	0658: 0	BEG52C	0934: 0	BUMP3	0632: 0	CDOVLY	0700: 0	CLEARH	0433: 0	CLR52	0201: 0
CLR52C	1696: 0	CONT	0568: 0	DOTS	0651: 0	DOWM	0520: 0	EXIT	0564: 0	GET	0497: 0
HALT	0563: 0	K9	0665: 0	KM10	0667: 0	KP1	0670: 0	KP15	0672: 0	KP2	0662: 0
KZ3	0661: 0	LOADNX	0700: 0	LOOP	0459: 0	MYSNAP	0333: 0	PHASID	0110: 0	PHASLD	0381: 0
PHS53S	0201: 0	RX1	0535: 0	SKIP	0621: 0	SNAPEX	0564: 0	SNAPSH	0333: 0	SX1	0416: 0
SX3	0409: 0	SXX	0402: 0	TOPCOR	0688: 0	TPERR	0728: 0	TPREAD	0704: 0	W2A	0664: 0
W2B	0669: 0	X1	0089: 0	X2	0094: 0	X3	0099: 0	XQTD	0680: 0		

## UNREFERENCED SYMBOLS

CDOVLY MYSNAP PHASLD SNAPEX TPERR TPREAD