October 28, 1964

MEMORANDUM TO: Users of 1401 Autocoder System

SUBJECT: 1401 Autocoder System, Program #1401-AU-037,
                Version 3, Modification Level 9

This modification letter has been prepared for distribution to all users of the 1401 Autocoder System and contains changes to Version 3, Modification Level 8 of that system.

The following minor restrictions are in effect:

Restrictions

The Branch if Minus (BM) instruction cannot be chained by the use of another BM with blank operands. A chain can be affected by using a BM xxx, yyy   followed by a BWZ (branch if wordmark or zone) or followed by the CHAIN macro.

A comments card without an asterisk will cause a 777 halt to occur.

There are no materials distributed with this letter.

Any changes in mailing address should be directed to the attention of the Manager of the Program Information Department, IBM Corporation
112 East Post Road, White Plains, New York.

This program has been registered by system tape and is listed under the name and address shown on your order. Program modifications as and when made by IBM will be sent to this same address. Should there be a change in your system type or in your address, or should you no longer need maintenance of this particular program, we would appreciate your notifying your IBM branch office.

PROGRAM INFORMATION DEPARTMENT

WSC - Bethesda
183/Systems & Procedures
November 10, 1964

MEMORANDUM

TO: Department 183

SUBJECT: Autocoder Processor Version 3, Level 9

The following restrictions may exist:

Group mark and tape mark literals will not be correctly assembled within the same overlay or program section. We suggest that group mark literals be declared as DCWs.

Negative address adjustment below zero will not be assembled correctly; i.e., there is no "wrap around" effect in address adjustment.

Blank origins following DAs may not be correctly assembled.

Comments cards appearing between the CTL and DIOCS cards will cause assembly errors during reassembly and regeneration of IOCS.

When re-assembling with alterations, insertion of instructions or constants immediately following sequence number 103 will not be assembled correctly.

When using the loadable tape option, signed DCW’s following DSA’s will not have the zone bits correctly assembled.

Indexing will be assembled on equated labels only as defined in the EQU statement itself. No indexing prevoiously defined on the symbol appearing in the operand of the EQU statement will be retained.

Columns 16 through 18 of a comments card (* in col. 6) may not contain END.

Negative character adjustment in the A operand will not be correctly assembled if the B operand is a group mark alphameric literal.

When using the loadable tape output option, area definition literals (#xx) of greater than 32 positions may not be correctly assembled.

3 and 4 character alphameric literals which are identical except for a double @ *i.e., @XXX@ and @XXX@@) can not be generated correctly by the processor.

When reassembling with alterations no instructions can be changed immediately after the last GENIO instruction.

When reassembling with alterations on IOCS programs, regeneration of IOCS can not be correctly completed if a previous reassembly has altered within the GENIO instructions.

The Branch if Minus (BM) instruction cannot be chained by the use of another BM with blank operands. A chain can be effected by using a BM xxx, yyy followed by a BWZ (branch if wordmark or zone) or followed by the CHAIN macro.

A comments card without an asterisk will cause a 777 halt to occur.

Comments cards should not follow an "ENT" statement as page overflow will be incorrect.

CALL or INCLD should not be punched in columns 16-19 or 16-20 respectively of a comments card.

If an adcon is used within a suffixed area, there should also be a LTORG in that suffixed area. If not, the adcon will be defined with an address in the last suffix area in which it appeared.

Alterations cannot be made to sequence numbers greater than 9999.

The label of an ORG statement immediately following an EQU*+1 will be assigned an address one greater than the address assigned the equate.

The highest core position may be exceeded without a flag if the operation code of the instruction falls within core, but the remainder goes over core.

Multiply defined tags will not be put out when iterations are made.

R. W. Weaver

RWW:cc