

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
101			JOB		FORTRAN COMPILER -- SORT TWO PHASE -- PHASE 05								
102			CTL		6611								
103			*										
104			*		SORT TWO PHASE: ADD THREE CHARACTERS TO EACH STATEMENT AND								
105			*		CHAIN STATEMENTS OF THE SAME TYPE TOGETHER, LEAVING THE								
106			*		ADDRESS OF THE FIRST STATEMENT OF EACH TYPE IN TYPTAB,								
107			*		WHICH STARTS AT 838.								
108			*		X1 HAS THE ADDRESS OF THE GROUP MARK WORD MARK AFTER (LOWER								
109			*		ADDRESS) THE LAST (LOWEST ADDRESS) STATEMENT.								
110			*										
111			X1	EQU	89						0089		
112			X2	EQU	94						0094		
113			X3	EQU	99						0099		
114			*										
115			*		STUFF IN THE RESIDENT AREA PHASE 0								
116			*										
117			PHASID	EQU	110						0110		
118			SNAPSH	EQU	333						0333		
119			TOPCOR	EQU	688						0688		
120			LOADNX	EQU	700						0700		
121			*										
122			*		IN PHASE 4								
123			*										
124			TYPTAB	EQU	759						0759		
125			*		INDEXED BY 30*(ZONE OF STATEMENT CODE) +								
126			*		3*(NUMERIC PART OF STATEMENT CODE). EACH								
127			*		ENTRY IS THE ADDRESS OF THE EARLIEST (HIGHEST								
128			*		ADDRESS) STATEMENT OF A TYPE. EACH STATEMENT								
129			*		HAS A POINTER TO THE NEXT ONE (LOWER IN CORE)								
130			*		OF THE SAME TYPE AS ITS FIRST THREE (HIGHEST								
131			*		ADDRESS) CHARACTERS.								
132			*										
133			*		GENERATE THE BLOCK TO LOAD PHASE 5								
134			*		IT ORIGINALLY STARTED BY EXECUTING SOME CLEAR-CORE CODE AT								
135			*		BEGINN-12 IN PHASE 4 THAT WASN'T OVERLAID. IT'S NOT								
136			*		CLEAR WHY THAT'S NEEDED AT ALL BECAUSE THE SAME AREA WAS								
137			*		CLEARED AT THE ENDS OF PHASES 3 AND 4.								
138			*										
139			PHAS5	NEWPH	@SORTER TWO@,LOADAD,BEGINN							MACRO	
			*									GEN	
			*		LOAD A NEW PHASE							GEN	
			*									GEN	
140			ORG		201						0201		
141			PHAS5	LCA)9N001,PHASID	PHASID	7	0201	L 288 110	GEN	1	288	110
142				BCE	LOADNX,LOADNX,1	Q: FROM CARDS?	8	0208	B 700 700 1	GEN	1	700	700
143				BCE	LOADNX,LOADNX,,	Q: FROM AUTOCODER TAPE?	8	0216	B 700 700 ,	GEN	1	700	700
144)0J001	ZA)9J001	CLEAR ERROR COUNTER	4	0224	? 278	GEN	1	278	
145)0K001	RTW	%U1,LOADAD	LOAD THE OVERLAY	8	0228	L %U1 22 R	GEN	1	%U1	1022
146				BER)0L001	Q: ERROR?	5	0236	B 248 L	GEN	2	248	
147				CS	BEGINN,)9P001	NO: ENTER THIS BLOCK	7	0241	/ 22 296	GEN	2	1022	296

SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS
)0J001	0224: 0)0J002	0201: 0)0K001	0228: 0)0K002	0225: 0)0L001	0248: 0)0L002	0244: 0
)0M002	0261: 0)0N002	0264: 0)0P002	0265: 0)0Q002	0271: 0)0R002	1099: 0)9J001	0278: 0
)9N001	0288: 0)9P001	0296: 0	BEGINN	1022: 0	CLRME	0201: 0	EOTWO	2000: 0	GM	1219: 0
GMWM	2008: 0	KP30	2007: 0	LOADAD	1022: 0	LOADNX	0700: 0	OVER	1157: 0	PHAS5	0201: 0
PHASID	0110: 0	SNAPSH	0333: 0	STMT	1919: 0	TOPCOR	0688: 0	TYPTAB	0759: 0	WORK6	2005: 0
X1	0089: 0	X2	0094: 0	X3	0099: 0						

UNREFERENCED SYMBOLS

GMWM