

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
101			JOB		FORTRAN COMPILER -- INSERT GROUP-MARK PHASE -- 07								
102			CTL		6611								
103			*										
104			*		REPLACE THE COLON (5-8) THAT SEPARATES EACH STATEMENT FROM								
105			*		ITS APPENDAGE (PREFIX) BY A GROUP MARK WITH A WORD MARK.								
106			*		REPLACE INTEGER MODULUS BY 05 IF IT'S ZERO.								
107			*		REPLACE MANTISSA DIGITS BY 08 IF IT'S ZERO.								
108			*		81-83 = START (TOP ADDRESS) OF FIRST (TOP IN MEMORY)								
109			*		STATEMENT. REMEMBER, STATEMENTS ARE SORTED BY TYPE NOW.								
110			*										
111			X1	EQU	89			0089					
112			X2	EQU	94			0094					
113			X3	EQU	99			0099					
114			*										
115			*		STUFF IN THE RESIDENT AREA								
116			*										
117			PHASID	EQU	110			0110					
118			SNAPSH	EQU	333			0333					
119			TOPCOR	EQU	688			0688					
120			IMOD	EQU	690			0690					
121			MANTIS	EQU	692			0692					
122			LOADNX	EQU	700			0700					
123			*										
124			110	DCW	@GROUP MARK@	10	0110				1		
125			089	DCW	000	3	0089				2		
126			091	DC	00	2	0091				2		
127			094	DCW	000	3	0094				2		
128			096	DC	00	2	0096				2		
129			*										
130			*		GENERATE THE BLOCK TO LOAD PHASE 6								
131			*										
132			PHAS7	NEWPH	@GROUP MARK@,LOADAD,BEGINN								MACRO
			*										GEN
			*		LOAD A NEW PHASE								GEN
			*										GEN
133			ORG		201			0201					
134			PHAS7	LCA)9N001,PHASID PHASID	7	0201	L 288 110	GEN	3	288	110	
135				BCE	LOADNX,LOADNX,1 Q: FROM CARDS?	8	0208	B 700 700 1	GEN	3	700	700	
136				BCE	LOADNX,LOADNX,, Q: FROM AUTOCODER TAPE?	8	0216	B 700 700 ,	GEN	3	700	700	
137)0J001	ZA)9J001 CLEAR ERROR COUNTER	4	0224	? 278	GEN	3	278		
138)0K001	RTW	%U1,LOADAD LOAD THE OVERLAY	8	0228	L %U1 756 R	GEN	3	%U1	756	
139				BER)0L001 Q: ERROR?	5	0236	B 248 L	GEN	4	248		
140				CS	BEGINN,)9P001 NO: ENTER THIS BLOCK	7	0241	/ 756 296	GEN	4	756	296	
141)0L001	BSP	1	5	0248	U %U1 B	GEN	4	%U1		
142				A	*-6,)9J001 BUMP ERROR COUNT	7	0253	A 253 278	GEN	4	253	278	
143				BCE)0K001,)9J001-1,0 Q: NOT TEN YET?	8	0260	B 228 277 0	GEN	4	228	277	
144				NOP	3333	4	0268	N C33	GEN	4	3333		
145				H		1	0272	.	GEN	4			
146				B)0J001	4	0273	B 224	GEN	5	224		
147)9J001	DCW	#2 ERROR COUNTER	2	0278		GEN	5			

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148)9N001	DCW	@GROUP MARK@ PHASE ID FROM PARAMETER 1	10		0288		GEN	5		
149			DC	@ NEWPH @	IDENTIFY IN A DECK, TAPE, OR DUMP	7		0295		GEN	5		
150)9P001	DCW	@}@	1		0296		GEN	5		
			* 108	DSA	PHAS7 NEWP LOAD ADDRESS FOR COMPILER-GEN					GEN			
151			XFR	PHAS7	PROHIBITED IN A MACRO				B 201		5	201	
152			*										
153				ORG	756				0756				
154			LOADAD	EQU	*&1 LOAD ADDRESS				0756				
155			BEGINN	MCW	83,X1	7		0756	M 083 089		6	083	089
156				SW	GM	4		0763	, 995		6	995	
157			LOOP	BCE	COLON,0&X1, :	8		0767	B 799 0 0 :		6	799	000+1
158			SWITCH	BCE	DONE,0&X1, NOP IF WORKING ON FORMAT	8		0775	B 867 0 0		6	867	000+1
159				BCE	SEEGM,0&X1, }	8		0783	B 822 0 0 } GMARK		6	822	000+1
160				SBR	X1	4		0791	H 089		6	089	
161				B	LOOP	4		0795	B 767		7	767	
162			COLON	LCA	GM,0&X1 REPLACE COLON BY GMWM	7		0799	L 995 0 0		7	995	000+1
163				SBR	X1 GET BELOW COLON	4		0806	H 089		7	089	
164				C	0&X1 AND THEN	4		0810	C 0 0		7	000+1	
165				SAR	X1 BELOW BOTTOM WORD MARK	4		0814	Q 089		7	089	
166				B	LOOP PROCESS NEXT STATEMENT	4		0818	B 767		7	767	
167			SEEGM	MCW	0&X1,PREFIX	7		0822	M 0 0 00		7	000+1	1000
168				BCE	FORMAT,PREFIX-4,F FORMAT STATEMENT?	8		0829	B 856 996 F		8	856	996
169				MCW	BRANCH,SWITCH	7		0837	M 01 775		8	1001	775
170			NEXT	MN	0&X1 DECREASE X1	4		0844	D 0 0		8	000+1	
171				SBR	X1 TO NEXT STATEMENT	4		0848	H 089		8	089	
172				B	LOOP	4		0852	B 767		8	767	
173			FORMAT	MCW	NOP,SWITCH	7		0856	M 02 775		8	1002	775
174				B	NEXT	4		0863	B 844		8	844	
175			*										
176			*	CLEAR FROM TOP CORE DOWN TO TOP OF STATEMENTS & X00									
177			*										
178			DONE	MCW	TOPCOR,X2	7		0867	M 688 094		9	688	094
179				MZ	83,K999 COMPUTE TOP	7		0874	Y 083 994		9	083	994
180				MZ	OF STATEMENTS	1		0881	Y		9		
181				MCW	& X00	1		0882	M		9		
182			CLEAR	CS	0&X2	4		0883	/ 0 0		9	000+2	
183				SBR	X2	4		0887	H 094		9	094	
184				C	X2,K999	7		0891	C 094 994		9	094	994
185				BU	CLEAR	5		0898	B 883 /		10	883	
186			*										
187			*	CLEAR FROM TOP OF STATEMENTS & X00 TO TOP OF STATEMENTS									
188			*										
189			CLEAR2	C	83,X2	7		0903	C 083 094		10	083	094
190				BE	DONE2	5		0910	B 934 S		10	934	
191				MCW	BLANK,0&X2	7		0915	M 03 0 0		10	1003	000+2
192				CW	0&X2	4		0922) 0 0		10	000+2	
193				SBR	X2	4		0926	H 094		10	094	
194				B	CLEAR2	4		0930	B 903		10	903	
195			DONE2	SW	IMOD-1	4		0934	, 689		11	689	
196				A	BLANK,MANTIS	7		0938	A 03 692		11	1003	692

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
197				C	IMOD,KZ2			0945	C 690 05		11	690	1005
198				BU	NOTZI			0952	B 964 /		11	964	
199				MCW	K05,IMOD			0957	M 07 690		11	1007	690
200			NOTZI	C	MANTIS,KZ2			0964	C 692 05		11	692	1005
201				BU	NOTZF			0971	B 983 /		12	983	
202				MCW	K08,MANTIS			0976	M 09 692		12	1009	692
203				*									
204				*	LOAD NEXT OVERLAY								
205				*									
206				NOTZF	BSS			0983	B 333 C		12	333	
207				B	LOADNX			0988	B 700		12	700	
208			K999	DCW	999			0994			12		
209			GM	DC	@}@			0995		GMARK	12		
210				PREFIX	DCW			1000			12		
211				BRANCH	B			1001	B		12		
212				NOP	NOP			1002	N		13		
213				BLANK	DCW			1003			13		
214				KZ2	DCW			1005			13		
215				K05	DCW			1007			13		
216				K08	DCW			1009			13		
217				GMWM	DCW			1010		GMARK	13		
218					ORG				0201				
219				*	DSA LOADAD								
220				XFR	BEGINN				B 756		13	756	
221				*									
222				*	CLEAR CORE AFTER THE PHASE USING MY CLRTOP ADDRESS								
223				*									
224				CLRME	CLRA					MACRO			
				*						GEN			
				*	CLEAR CORE AFTER A PHASE USING THE CLRTOP ADDRESS					GEN			
				*						GEN			
225					ORG				0800				
226)0R002	EQU			0799		GEN			
227					ORG				0201				
				*						GEN			
				*	CLEAR DOWN TO CLRBOT & X00 THE EASY WAY					GEN			
				*						GEN			
228				CLRME	EQU			0201		GEN			
229)0J002	CS			0201	/ 10	GEN	14	1010	
230					SBR			0205	H 204	GEN	14	204	
231					SBR			0209	H 250	GEN	14	250	
232					C			0213	C 204 261	GEN	14	204	261
233					BU			0220	B 201 /	GEN	14	201	
				*						GEN			
				*	NOW CLEAR DOWN TO CLRBOT THE HARD WAY					GEN			
				*						GEN			
234)0K002	C			0225	C 250 264	GEN	14	250	264
235					BU			0232	B 244 /	GEN	14	244	
236					CS			0237	/ 700 271	GEN	15	700	271
237)0L002	LCA			0244	L 265 000	GEN	15	265	000

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238				SBR)0L002&6	4		0251	H 250	GEN	15	250	
239				B)0K002	4		0255	B 225	GEN	15	225	
240)0M002	DSA)0R002 CLRBOT & X00 - 1	3		0261	799	GEN	15	799	
241)0N002	DSA	BEGINN CLRBOT	3		0264	756	GEN	15	756	
242)0P002	DCW	#1	1		0265		GEN	15		
243				DC	@CLRA @ IDENTIFY IN A DECK, TAPE, OR DUMP	5		0270		GEN	15		
244)0Q002	DCW	@}@	1		0271		GEN	16		
245				ORG	*&1 START NEW CARD FOR COMPILER-GEN				0273				
			*	DSA	CLRME CLRA					GEN			
246				XFR	CLRME PROHIBITED IN THE MACRO				B 201		16	201	

SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS
)0J001	0224: 0)0J002	0201: 0)0K001	0228: 0)0K002	0225: 0)0L001	0248: 0)0L002	0244: 0
)0M002	0261: 0)0N002	0264: 0)0P002	0265: 0)0Q002	0271: 0)0R002	0799: 0)9J001	0278: 0
)9N001	0288: 0)9P001	0296: 0	BEGINN	0756: 0	BLANK	1003: 0	BRANCH	1001: 0	CLEAR	0883: 0
CLEAR2	0903: 0	CLRME	0201: 0	COLON	0799: 0	DONE	0867: 0	DONE2	0934: 0	FORMAT	0856: 0
GM	0995: 0	GMWM	1010: 0	IMOD	0690: 0	K05	1007: 0	K08	1009: 0	K999	0994: 0
KZ2	1005: 0	LOADAD	0756: 0	LOADNX	0700: 0	LOOP	0767: 0	MANTIS	0692: 0	NEXT	0844: 0
NOP	1002: 0	NOTZF	0983: 0	NOTZI	0964: 0	PHAS7	0201: 0	PHASID	0110: 0	PREFIX	1000: 0
SEEGM	0822: 0	SNAPSH	0333: 0	SWITCH	0775: 0	TOPCOR	0688: 0	X1	0089: 0	X2	0094: 0
X3	0099: 0										

UNREFERENCED SYMBOLS

X3