

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
101				JOB	FORTRAN COMPILER -- LOAD PHASES 52BC -- PHASE 52A								
102				CTL	6611								
103				*									
104				*	EXTERNALLY REFERENCED SYMBOLS ARE MARKED WITH ASTERISK IN COLUMN 1.								
105				*									
106				*	AS THE OBJECT CODING MAY ORIGINATE AT 1697, THE CODING FOR								
107				*	PHASE 52 MUST BE SPLIT INTO TWO PARTS, THE FIRST OF WHICH								
108				*	REPLACES THE SNAPSHOT CODING IN POSITIONS 333-680. ORIGINALLY,								
109				*	THIS PHASE LOADED THE TWO SECTIONS BUT NOW THAT'S DONE IN								
110				*	PHASE 52 B&C. THIS PHASE SIMPLY ADJUSTS X3 ACCORDING TO THE								
111				*	PATCH IN V3M4.								
112				*									
113				X3	EQU 99			0099					
114				*									
115				*	STUFF IN THE RESIDENT AREA								
116				*									
117				PHASID	EQU 110 PHASE ID, FOR SNAPSHOT DUMPS			0110					
118				*									
119				EXT00	SNAPSH, LOADNX, CDOVLY					MACRO			
120				SNAPSH	EQU 333			0333		GEN			
121				PHASLD	EQU 381			0381		GEN			
122				SNAPEX	EQU 564			0564		GEN			
123				LOADNX	EQU 700 CARD OVERLAY UNLESS NOP			0700		GEN			
124				CDOVLY	EQU 700 1 IF LOADING FROM CARDS, N IF FROM TAPE			0700		GEN			
125				TPREAD	EQU 704 LOAD OVERLAY FROM TAPE			0704		GEN			
126				TPERR	EQU 728			0728		GEN			
127				*									
128				EXT03	START, TOP OF PHASE 3					MACRO			
129				BEGIN3	EQU 838			0838		GEN			
130				TOP3	EQU 2600			2600		GEN			
131				XT54C	STUFF IN THE RUNTIME FORMAT ROUTINE 54C					MACRO			
132				FMTBAS	EQU 1697			1697		GEN			
133				RELENT	EQU 2132 ENTER HERE FROM RELOCATABLE FUNCTION TABLE			2132		GEN			
134				NOOVFL	EQU 3138			3138		GEN			
135				NGM	EQU 4279			4279		GEN			
136				AFMT1	EQU 4280			4280		GEN			
137				AGM	EQU 4616			4616		GEN			
138				*									
139				PHS52A	LDPH LOAD 52B&C,LOADAD,BEG52A,,,52A					MACRO			
				*	PHAZ LDPH [PHASID],LOADAD,ENTAD[,SKIPFG,SKIP],[NUMBER][,HALT]					GEN			
				*	XFR PHASZ PROHIBITED IN A MACRO					GEN			
				*						GEN			
				*	LOAD A BLOCK					GEN			
				*						GEN			
140				)6J004	EQU 110 PHASE ID			0110		GEN			
141				)6K004	EQU 700 LOAD NEXT PHASE			0700		GEN			
142				)6L004	EQU 704 TAPE READ INSTRUCTION			0704		GEN			
143				)6M004	EQU 728 TAPE ERROR HANDLER			0728		GEN			
144				*						GEN			
				ORG	201			0201					



SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
195				*									
196				*	ADJUST X3, THEN LOAD PHASES 52B AND 52C								
197				*									
198	*	934	BEG52A	BWZ	*&5,X3,2			V3M4	8 0934	V 946 099 2	8	946	099
199	1	044		B	LOADBC			V3M4	4 0942	B 965	8	965	
200	1	048		BWZ	*&5,X3-2,S			V3M4	8 0946	V 958 097 S	8	958	097
201	1	056		B	LOADBC			V3M4	4 0954	B 965	9	965	
202	1	060		SBR	X3,2000			V3M4	7 0958	H 099 !00	9	099	2000
203			LOADBC	BIN	SNAPSH,C				5 0965	B 333 C	9	333	
204	1	116		BIN	LOADNX,			V3M4	5 0970	B 700	9	700	
205				ORG	1696 MOKOTOFF V3M4.LST LINE 10515					1696			
206	1	696	GMWM	DCW	@}@				1 1696		10		
207				XFR	BEG52A					B 934	11	934	
208			CLRME	CLRA	BEG52A,GMWM								
			*	CLRA	CLRBOT,CLRTOP[,ORG,GMWMAD]								
			*										
			*		CLEAR CORE AFTER A PHASE USING THE CLRTOP ADDRESS								
			*										
209				ORG	201					0201			
			*										
			*		CLEAR DOWN TO CLRBOT & X00 THE EASY WAY								
			*										
210			CLRME	EQU	*&1					0201			
211			)0J005	CS	GMWM CLEAR FROM CLRTOP				4 0201	/ W96	12	1696	
212				SBR	)0J005&3				4 0205	H 204	12	204	
213				SBR	)0L005&6				4 0209	H 250	12	250	
214				C	)0J005&3,)0M005 DOWN TO CLRBOT & X00?				7 0213	C 204 261	12	204	261
215				BU	)0J005				5 0220	B 201 /	12	201	
			*										
			*		NOW CLEAR DOWN TO CLRBOT THE HARD WAY								
			*										
216			)0K005	C	)0L005&6,)0N005				7 0225	C 250 264	12	250	264
217				BU	)0L005				5 0232	B 244 /	12	244	
218				CS	LOADNX,)0Q005 LOAD THE NEXT BLOCK AT 1				7 0237	/ 700 271	13	700	271
219			)0L005	LCA	)0P005,0-0 CLEAR WITH BLANK AND WORD MARK				7 0244	L 265 000	13	265	000
220				SBR	)0L005&6				4 0251	H 250	13	250	
221				B	)0K005				4 0255	B 225	13	225	
222			)0M005	DSA	)0R005 CLRBOT & X00 - 1				3 0261	999	13	999	
223			)0N005	DSA	BEG52A CLRBOT				3 0264	934	13	934	
224			)0P005	DCW	#1				1 0265		13		
225				DC	@CLRA @ IDENTIFY IN A DECK, TAPE, OR DUMP				5 0270		13		
226			)0Q005	DCW	@}@				1 0271		14		
227				ORG	BEG52A&X00					1000			
228			)0R005	EQU	* CLRBOT & X00 - 1					0999			
229				XFR	CLRME					B 201	15	201	

SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS
)OJ005	0201: 0	)OK005	0225: 0	)OL005	0244: 0	)OM005	0261: 0	)ON005	0264: 0	)OP005	0265: 0
)OQ005	0271: 0	)OR005	0999: 0	)6J004	0110: 0	)6K004	0700: 0	)6L004	0704: 0	)6M004	0728: 0
)9J004	0253: 0	)9R004	0258: 0	ABSVAL	0891: 0	AFMT1	4280: 0	AGM	4616: 0	ARYBOT	0933: 0
ATANFN	0894: 0	BEG52A	0934: 0	BEGIN3	0838: 0	CDOVLY	0700: 0	CLRME	0201: 0	COMFN1	0906: 0
CONBOT	0930: 0	DOADR1	0924: 0	DOADR2	0921: 0	DOADR3	0918: 0	DOINIT	0915: 0	EXLINK	0840: 0
FIXFUN	0885: 0	FLTFUN	0882: 0	FMTBAS	1697: 0	FUNTAB	0924: 0	GMWM	1696: 0	LOADAD	0838: 0
LOADBC	0965: 0	LOADNX	0700: 0	LOGFUN	0900: 0	NEGTFN	0888: 0	NGM	4279: 0	NOOVFL	3138: 0
OBLIST	0912: 0	PHASID	0110: 0	PHASLD	0381: 0	PHS52A	0201: 0	RELENT	2132: 0	SINFUN	0903: 0
SNAPEX	0564: 0	SNAPSH	0333: 0	SQRTFN	0879: 0	SUBSC	0909: 0	SX2	0927: 0	TOP3	2600: 0
TPERR	0728: 0	TPREAD	0704: 0	USER1	0876: 0	X3	0099: 0	XPNETL	0897: 0	YUSER2	0873: 0
YUSER3	0870: 0	YUSER4	0867: 0	YUSER5	0864: 0	YUSER6	0861: 0	YUSER7	0858: 0	YUSER8	0855: 0
YUSER9	0852: 0	YUSR10	0849: 0	YUSR11	0846: 0	YUSR12	0843: 0				

## UNREFERENCED SYMBOLS

ABSVAL AFMT1 AGM ARYBOT ATANFN CDOVLY COMFN1 CONBOT DOADR2 DOADR3 DOINIT EXLINK FIXFUN FLTFUN FMTBAS LOGFUN NEGTFN  
 NGM NOOVFL OBLIST PHASID PHASLD SINFUN SNAPEX SQRTFN SUBSC SX2 TOP3 TPERR TPREAD USER1 XPNETL YUSER2 YUSER3  
 YUSER4 YUSER5 YUSER6 YUSER7 YUSER8 YUSER9 YUSR10 YUSR11 YUSR12