

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
101			JOB		FORTRAN COMPILER -- CONDENSED DECK PHASE ONE -- 57								
102			CTL		6611								
103			*										
104			*		WHEN REQUESTED (IF THERE ARE NO INPUT ERRORS), THE COMILER								
105			*		WILL PUNCH A SELF-LOADING CARD DECK. THE DECK IS LISTED ON								
106			*		THE PRINTER IF SENSE SWITCH B IS ON. THIS PHASE PUNCHES								
107			*		ONLY THE CLEAR-STORAGE AND BOOTSTRAP CARDS.								
108			*										
109			*		STUFF IN THE RESIDENT AREA								
110			*										
111			PHASID	EQU	110 PHASE ID, FOR SNAPSHOT DUMPS			0110					
112			GLOBER	EQU	184 GLOBAL ERROR FLAG -- WM MEANS ERROR			0184					
113			ARYTOP	EQU	194 TOP OF ARRAYS IN OBJECT CODE			0194					
114			TOPCOR	EQU	688 TOP CORE ADDRESS FROM PARAM CARD			0688					
115			CONDNS	EQU	693 P FOR CONDENSED DECK			0693					
116			*										
117			EXT00		SNAPSH, LOADNX, CDOVLY					MACRO			
118			SNAPSH	EQU	333			0333		GEN			
119			PHASLD	EQU	381			0381		GEN			
120			SNAPEX	EQU	564			0564		GEN			
121			LOADNX	EQU	700 CARD OVERLAY UNLESS NOP			0700		GEN			
122			CDOVLY	EQU	700 1 IF LOADING FROM CARDS, N IF FROM TAPE			0700		GEN			
123			TPREAD	EQU	704 LOAD OVERLAY FROM TAPE			0704		GEN			
124			TPERR	EQU	728			0728		GEN			
125			EXT03		START, TOP OF PHASE 3					MACRO			
126			BEGIN3	EQU	838			0838		GEN			
127			TOP3	EQU	2600			2600		GEN			
128			*										
129			PHAS57	LDPH	CONDECK1,LOADAD,BEGN57,,,57					MACRO			
			*	PHAZ	LDPH [PHASID],LOADAD,ENTAD[,SKIPFG,SKIP],[NUMBER][,HALT]					GEN			
			*	XFR	PHASZ PROHIBITED IN A MACRO					GEN			
			*							GEN			
			*	LOAD	A BLOCK					GEN			
			*							GEN			
130)6J003	EQU	110 PHASE ID			0110		GEN			
131)6K003	EQU	700 LOAD NEXT PHASE			0700		GEN			
132)6L003	EQU	704 TAPE READ INSTRUCTION			0704		GEN			
133)6M003	EQU	728 TAPE ERROR HANDLER			0728		GEN			
			*							GEN			
134			ORG		201				0201				
135			PHAS57	EQU	*&1			0201		GEN			
136			LCA)9J003,)6J003	7	0201	L 251 110	GEN	1	251	110	
137			BCE)6K003,)6K003,1 Q: LOADING FROM CARDS?	8	0208	B 700 700 1	GEN	1	700	700	
138			BCE)6K003,)6L003&4,0 Q: LOADING FROM AUTOCODER TAPE?	8	0216	B 700 708 0	GEN	1	700	708	
139			RTW	1,LOADAD	READ THE BLOCK	8	0224	L %U1 838 R	GEN	1	%U1	838	
140			BER)6M003	Q: TAPE ERROR?	5	0232	B 728 L	GEN	1	728		
141			CS	BEGN57,)9R003	ENTER THE BLOCK	7	0237	/ 883 255	GEN	2	883	255	
142)9J003	DCW	@CONDECK1@ PHASE ID	8	0251		GEN	2			
143			DC	#1		1	0252		GEN	2			
144			DC	@57@	PHASE NUMBER	2	0254		GEN	2			

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145)9R003	DCW	@}@		1	0255		GEN	2		
146				XFR	PHAS57				B 201		3	201	
147			*										
148				ORG	BEGIN3				0838				
149					* PUNCH ROUTINE IS USED IN PHASE 58								
150				LOADAD	EQU * &1			0838					
151	*	838		PUNCH	SBR PUNCHX&3	4		0838	H 858		4	858	
152		842		A	*-6,175 BUMP SEQUENCE NUMBER	7		0842	A 842 175		4	842	175
153		849		BSS	PUNCHW,B PUNCH AND PRINT?	5		0849	B 859 B		4	859	
154		854		P		1		0854	4		4		
155		855	PUNCHX	B	0-0	4		0855	B 000		4	000	
156		859	PUNCHW	MCW	180,280	7		0859	M 180 280		4	180	280
157		866		MCW		1		0866	M		4		
158		867		WP		1		0867	6		5		
159		868		BCV	* &5	5		0868	B 877 @		5	877	
160		873		B	* &3	4		0873	B 879		5	879	
161		877		CC	1	2		0877	F 1		5		
162		879		B	PUNCHX	4		0879	B 855		5	855	
163	*	883	BEGN57	BCE	* &5,CONDNS,P PUNCH CONDENSED DECK?	8		0883	B 895 693 P		5	895	693
164		891		B	DONE	4		0891	B 955		5	955	
165		895		BW	DONE,GLOBER	8		0895	V 955 184 1		6	955	184
166		903		LCA	ARYTOP,ARYTOP	7		0903	L 194 194		6	194	194
167		910		CS	180	4		0910	/ 180		6	180	
168		914		SW	101	4		0914	, 101		6	101	
169		918		MCW	685,180	7		0918	M 685 180		6	685	180
170		925		BSS	* &5,B	5		0925	B 934 B		6	934	
171		930		B	CONT	4		0930	B 964		6	964	
172		934		CC	1	2		0934	F 1		7		
173		936		CS	332	4		0936	/ 332		7	332	
174		940		CS		1		0940	/		7		
175		941		MCW	TITLE,260	7		0941	M T71 260		7	1371	260
176		948		W		1		0948	2		7		
177		949		CC	J	2		0949	F J		7		
178		951		B	CONT	4		0951	B 964		7	964	
179		955	DONE	BSS	SNAPSH,C	5		0955	B 333 C		8	333	
180		960		B	LOADNX	4		0960	B 700		8	700	
181		964	CONT	LCA	K0000,175 SEQUENCE NUMBER	7		0964	L T75 175		8	1375	175
182		971		BWZ	UNDER4,TOPCOR,2	8		0971	V 12 688 2		8	1012	688
183		979		MCW	B1G4K,152	7		0979	M S86 152		8	1286	152
184		986		B	PUNCH FIRST BOOTSTRAP GE 4K	4		0986	B 838		8	838	
185		990		MCW	TOPCOR,B2AG4K	7		0990	M 688 T15		9	688	1315
186		997		MCW	B2BG4K,171	7		0997	M T57 171		9	1357	171
187	1	004		B	PUNCH SECOND BOOTSTRAP GE 4K	4		1004	B 838		9	838	
188	1	008		B	THIRD	4		1008	B 41		9	1041	
189	1	012	UNDER4	MCW	B1L4K,144	7		1012	M /07 144		9	1107	144
190	1	019		B	PUNCH FIRST BOOTSTRAP LT 4K	4		1019	B 838		9	838	
191	1	023		MCW	TOPCOR,B2AL4K	7		1023	M 688 /36		10	688	1136
192	1	030		MCW	B2BL4K,170	7		1030	M /77 170		10	1177	170
193	1	037		B	PUNCH SECOND BOOTSTRAP LT 4K	4		1037	B 838		10	838	
194	1	041	THIRD	MCW	R40,171	7		1041	M /88 171		10	1188	171

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195	1	048		CS			1	1048	/		10		
196	1	049		LCA	B3,146		7	1049	L S34 146		10	1234	146
197	1	056		B	PUNCH THIRD BOOTSTRAP		4	1056	B 838		10	838	
198	1	060		B	DONE		4	1060	B 955		11	955	
199	1	107	B1L4K	DCW	@,008015,019026,030,034041,045,053,0570571026@		44	1107			13		
200	1	136	B2AL4K	DCW	@L068112,102106,113/101099/I99@		29	1136			13		
201	1	177	B2BL4K	DC	@,027A070028)027B0010270B0261,001/001113I0@		41	1177			15		
202	1	188	R40	DCW	@,0010011040@		11	1188			15		
203	1	234	B3	DCW	@,008015,022029,036040,047054,061068,072/061039@		46	1234			17		
204	1	277		DCW	@,008015,022026,030037,044,049,053034,035036@ V3M4		43	1277			19		
205	1	286	B1G4K	DC	@N00001026@		9	1286			19		
206	1	315	B2AG4K	DCW	@L068116,105106,110117B101/I9I@		29	1315			20		
207	1	357	B2BG4K	DC	@H029NNNC029056B026/B001/0991,001/001117I0?@ V3M4		42	1357			22		
208	1	371	TITLE	DCW	@CONDENSED DECK@		14	1371			22		
209	1	375	K0000	DCW	@0000@		4	1375			22		
210	1	376	GMWM	DCW	@}@		1	1376		GMARK	22		
211				XFR	BEGN57				B 883		23	883	
212			CLRME	CLRA	BEGN57,GMWM					MACRO			
			*	CLRA	CLRBOT,CLRTOP[,ORG,GMWMAD]					GEN			
			*							GEN			
			*		CLEAR CORE AFTER A PHASE USING THE CLRTOP ADDRESS					GEN			
			*							GEN			
213			ORG		201				0201				
			*							GEN			
			*		CLEAR DOWN TO CLRBOT & X00 THE EASY WAY					GEN			
			*							GEN			
214			CLRME	EQU	*&1			0201					
215)0J004	CS	GMWM CLEAR FROM CLRTOP		4	0201	/ T76	GEN	24	1376	
216				SBR)0J004&3		4	0205	H 204	GEN	24	204	
217				SBR)0L004&6		4	0209	H 250	GEN	24	250	
218				C)0J004&3,)0M004 DOWN TO CLRBOT & X00?		7	0213	C 204 261	GEN	24	204	261
219				BU)0J004		5	0220	B 201 /	GEN	24	201	
			*							GEN			
			*		NOW CLEAR DOWN TO CLRBOT THE HARD WAY					GEN			
			*							GEN			
220)0K004	C)0L004&6,)0N004		7	0225	C 250 264	GEN	24	250	264
221				BU)0L004		5	0232	B 244 /	GEN	24	244	
222				CS	LOADNX,)0Q004 LOAD THE NEXT BLOCK AT 1		7	0237	/ 700 271	GEN	25	700	271
223)0L004	LCA)0P004,0-0 CLEAR WITH BLANK AND WORD MARK		7	0244	L 265 000	GEN	25	265	000
224				SBR)0L004&6		4	0251	H 250	GEN	25	250	
225				B)0K004		4	0255	B 225	GEN	25	225	
226)0M004	DSA)0R004 CLRBOT & X00 - 1		3	0261	899	GEN	25	899	
227)0N004	DSA	BEGN57 CLRBOT		3	0264	883	GEN	25	883	
228)0P004	DCW	#1		1	0265		GEN	25		
229				DC	@CLRA @ IDENTIFY IN A DECK, TAPE, OR DUMP		5	0270		GEN	25		
230)0Q004	DCW	@}@		1	0271		GEN	26		
231				ORG	BEGN57&X00				0900				
232)0R004	EQU	* CLRBOT & X00 - 1			0899		GEN			
233				XFR	CLRME				B 201		27	201	

SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS
)0J004	0201: 0)0K004	0225: 0)0L004	0244: 0)0M004	0261: 0)0N004	0264: 0)0P004	0265: 0
)0Q004	0271: 0)0R004	0899: 0)6J003	0110: 0)6K003	0700: 0)6L003	0704: 0)6M003	0728: 0
)9J003	0251: 0)9R003	0255: 0	ARYTOP	0194: 0	B1G4K	1286: 0	B1L4K	1107: 0	B2AG4K	1315: 0
B2AL4K	1136: 0	B2BG4K	1357: 0	B2BL4K	1177: 0	B3	1234: 0	BEGIN3	0838: 0	BEGN57	0883: 0
CDOVLY	0700: 0	CLRME	0201: 0	CONDNS	0693: 0	CONT	0964: 0	DONE	0955: 0	GLOBER	0184: 0
GMWM	1376: 0	K0000	1375: 0	LOADAD	0838: 0	LOADNX	0700: 0	PHAS57	0201: 0	PHASID	0110: 0
PHASLD	0381: 0	PUNCH	0838: 0	PUNCHW	0859: 0	PUNCHX	0855: 0	R40	1188: 0	SNAPEX	0564: 0
SNAPSH	0333: 0	THIRD	1041: 0	TITLE	1371: 0	TOP3	2600: 0	TOPCOR	0688: 0	TPERR	0728: 0
TPREAD	0704: 0	UNDER4	1012: 0								

UNREFERENCED SYMBOLS

CDOVLY PHASID PHASLD SNAPEX TOP3 TPERR TPREAD