

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
101			JOB		FORTRAN COMPILER -- CONTINUE PHASE -- PHASE 45								
102			CTL		6611								
103			*										
104			*		NO OBJECT-TIME INSTRUCTIONS ARE GENERATED FOR THESE								
105			*		STATEMENTS. THIS PHASE PASSES INFORMATION REQUIRED BY								
106			*		THE RESORT PHASES OF THE COMPILER.								
107			*										
108			X1	EQU	89			0089					
109			X3	EQU	99			0099					
110			*										
111			*		STUFF IN THE RESIDENT AREA								
112			*										
113				EXT00	SNAPSH, LOADNX, CDOVLY					MACRO			
114			SNAPSH	EQU	333			0333		GEN			
115			PHASLD	EQU	381			0381		GEN			
116			SNAPEX	EQU	564			0564		GEN			
117			LOADNX	EQU	700			0700		GEN			
118			CDOVLY	EQU	700			0700		GEN			
119			TPREAD	EQU	704			0704		GEN			
120			TPERR	EQU	728			0728		GEN			
121			*										
122				EXT03	START, TOP OF PHASE 3					MACRO			
123			BEGIN3	EQU	838			0838		GEN			
124			TOP3	EQU	2600			2600		GEN			
125			*										
126			PHAS45	LDPH	CONTINUE,LOADAD,BEGN45,,45					MACRO			
			*	PHAZ	LDPH [PHASID],LOADAD,ENTAD[,SKIPFG,SKIP],[NUMBER][,HALT]					GEN			
			*	XFR	PHASZ PROHIBITED IN A MACRO					GEN			
			*							GEN			
			*	LOAD	A BLOCK					GEN			
			*							GEN			
127			)6J003	EQU	110			0110		GEN			
128			)6K003	EQU	700			0700		GEN			
129			)6L003	EQU	704			0704		GEN			
130			)6M003	EQU	728			0728		GEN			
			*							GEN			
131				ORG	201				0201				
132			PHAS45	BSS	)8J003,G		5	0201	B 257 G	GEN	1	257	
133				NOP	TO PATCH IN TRAPS FOR DEBUGGING		1	0206	N	GEN	1		
134			)0J003	EQU	*&1			0207		GEN			
135				LCA	)9J003,)6J003		7	0207	L 280 110	GEN	1	280	110
136				BCE	)1J003,)6K003,1		8	0214	B 250 700 1	GEN	1	250	700
137				BCE	)1J003,)6L003&4,0		8	0222	B 250 708 0	GEN	1	250	708
138				RTW	1,LOADAD		8	0230	L %U1 838 R	GEN	1	%U1	838
139				BER	)6M003		5	0238	B 728 L	GEN	2	728	
140				CS	BEGN45,)9R003		7	0243	/ 838 284	GEN	2	838	284
141			)1J003	CS	)6K003,)9R003		7	0250	/ 700 284	GEN	2	700	284
142			)8J003	SW	)9R003		4	0257	, 284	GEN	2	284	
143				MU	%T0,)8K003,W		8	0261	M %T0 273 W	GEN	2	%T0	273
144				H	)0J003		4	0269	. 207	GEN	2	207	

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145			)8K003	EQU	*&1			0273		GEN			
146			)9J003	DCW	@CONTINUE@ PHASE ID	8		0280		GEN	3		
147				DCW	#1	1		0281		GEN	3		
148				DC	@45@ PHASE NUMBER	2		0283		GEN	3		
149			)9R003	DCW	@}@	1		0284		GEN	3		
150				XFR	PHAS45				B 201		4	201	
151			*										
152				ORG	BEGIN3				0838				
153			LOADAD	EQU	*&1 LOAD ADDRESS			0838					
154	838		BEGN45	BCE	DONE,0&X1,	8		0838	B 861 0 0		5	861	000+1
155	846			MCW	0&X1,CODSEQ	7		0846	M 0 0 906		5	000+1	906
156	853			BCE	CONT,CODSEQ-3,C CONTINUE STATEMENT?	8		0853	B 865 903 C		5	865	903
157	861		DONE	B	LOADNX	4		0861	B 700		5	700	
158	884		CONT	LCA	0&X1,0&X3	7		0865	L 0 0 0?0		5	000+1	000+3
159	891			SAR	X1	4		0872	Q 089		5	089	
160	895			C	0&X3	4		0876	C 0?0		6	000+3	
161	899			SAR	X3	4		0880	Q 099		6	099	
162	903			LCA	1&X1,2&X3 REPLACE STATEMENT CODE BY GMWM	7		0884	L 0 1 0?2		6	001+1	002+3
163	910			C	0&X1	4		0891	C 0 0		6	000+1	
164	914			SAR	X1	4		0895	Q 089		6	089	
165	918			B	BEGN45	4		0899	B 838		6	838	
166			*										
167			* DATA										
168			*										
169	925		CODSEQ	DCW	#4	4		0906			6		
170	931		GMWM	DCW	@}@	1		0907		GMARK	7		
171				EX	BEGN45				B 838		8	838	
172			CLRME	CLRA	LOADAD,GMWM,C					MACRO			
			*	CLRA	CLRBOT,CLRTOP[,SS,HERE,GWMAD]					GEN			
			*							GEN			
			* CLEAR CORE AFTER A PHASE USING THE CLRTOP ADDRESS							GEN			
			*							GEN			
173				ORG	201				0201				
			*							GEN			
			* CLEAR DOWN TO CLRBOT & X00 THE EASY WAY							GEN			
			*							GEN			
174			CLRME	EQU	*&1			0201		GEN			
175				BSS	SNAPSH,C	5		0201	B 333 C		9	333	
176			)0J004	CS	GMWM CLEAR FROM CLRTOP	4		0206	/ 907		9	907	
177				SBR	)0J004&3	4		0210	H 209		9	209	
178				SBR	)0L004&6	4		0214	H 255		9	255	
179				C	)0J004&3,)0M004 DOWN TO CLRBOT & X00?	7		0218	C 209 266		9	209	266
180				BU	)0J004	5		0225	B 206 /		9	206	
			*							GEN			
			* NOW CLEAR DOWN TO CLRBOT THE HARD WAY							GEN			
			*							GEN			
181			)0K004	C	)0L004&6,)0N004	7		0230	C 255 269		9	255	269
182				BU	)0L004	5		0237	B 249 /		10	249	
183				CS	LOADNX,)0Q004 LOAD THE NEXT BLOCK AT 1	7		0242	/ 700 276		10	700	276
184			)0L004	LCA	)0P004,0-0 CLEAR WITH BLANK AND WORD MARK	7		0249	L 270 000		10	270	000

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
185				SBR	)0L004&6	4		0256	H 255	GEN	10	255	
186				B	)0K004	4		0260	B 230	GEN	10	230	
187			)0M004	DSA	)0R004 CLRBOT & X00 - 1	3		0266	899	GEN	10	899	
188			)0N004	DSA	LOADAD CLRBOT	3		0269	838	GEN	10	838	
189			)0P004	DCW	#1	1		0270		GEN	11		
190				DC	@CLRA @ IDENTIFY IN A DECK, TAPE, OR DUMP	5		0275		GEN	11		
191			)0Q004	DCW	@}@	1		0276		GEN	11		
192				ORG	LOADAD&X00				0900				
193			)0R004	EQU	* CLRBOT & X00 - 1			0899		GEN			
194				XFR	CLRME				B 201		12	201	

SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS
)0J003	0207: 0	)0J004	0206: 0	)0K004	0230: 0	)0L004	0249: 0	)0M004	0266: 0	)0N004	0269: 0
)0P004	0270: 0	)0Q004	0276: 0	)0R004	0899: 0	)1J003	0250: 0	)6J003	0110: 0	)6K003	0700: 0
)6L003	0704: 0	)6M003	0728: 0	)8J003	0257: 0	)8K003	0273: 0	)9J003	0280: 0	)9R003	0284: 0
BEGIN3	0838: 0	BEGN45	0838: 0	CDOVLY	0700: 0	CLRME	0201: 0	CODSEQ	0906: 0	CONT	0865: 0
DONE	0861: 0	GMWM	0907: 0	LOADAD	0838: 0	LOADNX	0700: 0	PHAS45	0201: 0	PHASLD	0381: 0
SNAPEX	0564: 0	SNAPSH	0333: 0	TOP3	2600: 0	TPERR	0728: 0	TPREAD	0704: 0	X1	0089: 0
X3	0099: 0										

## UNREFERENCED SYMBOLS

CDOVLY PHASLD SNAPEX TOP3 TPERR TPREAD