

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
101			JOB		FORTRAN COMPILER -- FORMAT LOADER -- PHASE 54A								
102			CTL		6611								
103			*										
104			*		THIS PHASE SELECTS THE PROPER I/O ROUTINE AND LOADS IT INTO								
105			*		ITS OBJECT CORE-STORAGE LOCATION.								
106			*										
107			*		LIMITED FORMAT ROUTINE IS FIRST (54B), NORMAL FORMAT ROUTINE								
108			*		IS SECOND (54C), A-CONVERSION FORMAT ROUTINE IS THIRD (54D).								
109			*										
110			*		STUFF IN THE RESIDENT AREA								
111			*										
112			FMTSW	EQU	696 X FOR NO FORMAT, L FOR LIMITED FORMAT			0696					
113			*		BLANK FOR ORDINARY, A FOR A CONVERSION								
114			*										
115					EXT00 SNAPSH, LOADNX, CDOVLY								MACRO
116			SNAPSH	EQU	333			0333					GEN
117			PHASLD	EQU	381			0381					GEN
118			SNAPEX	EQU	564			0564					GEN
119			LOADNX	EQU	700 CARD OVERLAY UNLESS NOP			0700					GEN
120			CDOVLY	EQU	700 1 IF LOADING FROM CARDS, N IF FROM TAPE			0700					GEN
121			TPREAD	EQU	704 LOAD OVERLAY FROM TAPE			0704					GEN
122			TPERR	EQU	728			0728					GEN
123			*										
124					XT52B STUFF IN PHASE 52B -- FUNCTION LOADER								MACRO
125			BEG52C	EQU	934 RELOCATION TAG FROM SW INSTRUCTION			0934					GEN
126			CLR52C	EQU	1696			1696					GEN
127					XT54B STUFF IN PHASE 54B -- LIMITED FORMAT								MACRO
128			FMTBAS	EQU	1697			1697					GEN
129			LIMADR	EQU	2015 USED IN DIMENSION PHASE 2			2015					GEN
130			LGM	EQU	2031			2031					GEN
131			*										
132			PHAS54	LDPH	FORMATPAK, LOAD54, BEGN54, , , 54A								MACRO
			*	PHAZ	LDPH [PHASID], LOADAD, ENTAD[, SKIPFG, SKIP], [NUMBER] [, HALT]								GEN
			*	XFR	PHASZ PROHIBITED IN A MACRO								GEN
			*										GEN
			*	LOAD	A BLOCK								GEN
			*										GEN
133)6J004	EQU	110 PHASE ID			0110					GEN
134)6K004	EQU	700 LOAD NEXT PHASE			0700					GEN
135)6L004	EQU	704 TAPE READ INSTRUCTION			0704					GEN
136)6M004	EQU	728 TAPE ERROR HANDLER			0728					GEN
			*										GEN
137				ORG	201				0201				
138			PHAS54	BSS)8J004,G		5	0201	B 257 G	GEN	1	257	
139				NOP	TO PATCH IN TRAPS FOR DEBUGGING		1	0206	N	GEN	1		
140)0J004	EQU	*&1			0207		GEN			
141				LCA)9J004,)6J004		7	0207	L 281 110	GEN	1	281	110
142				BCE)1J004,)6K004,1 Q: LOADING FROM CARDS?		8	0214	B 250 700 1	GEN	1	250	700
143				BCE)1J004,)6L004&4,0 Q: LOADING FROM AUTOCODER TAPE?		8	0222	B 250 708 0	GEN	1	250	708
144				RTW	1,LOAD54 READ THE BLOCK		8	0230	L %U1 934 R	GEN	1	%U1	934

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION	TYPE	CARD	A-ADDR	B-ADDR
195			SKIPTP	RTW	1,1			8	1045	L %U1 001 R		8	%U1 001
196				BER	TPERR			5	1053	B 728 L		8	728
197				BCE	LOADNX,8,B			8	1058	B 700 008 B		8	700 008
198				B	SKIPTP			4	1066	B 45		8	1045
199				*									
200				*	DATA								
201				*									
202	1	253	GMWM	DCW	@}@			1	1070		GMARK	8	
203	*		GMWM54	EQU	GMWM				1070				
204				*									
205				*	WE CAN'T PUT THE OVERLAY TO CLEAR PHASE 54A HERE BECAUSE								
206				*	PHASE 54A SKIPS OR LOADS PHASES 54BCD. THE CLEAR CODE IS IN								
207				*	PHASE 55. AND WE CAN'T PUT THIS COMMENT AFTER THE XFR BECAUSE								
208				*	VAN'S AUTOCODER GENERATES AN EXTRA AUTOCODER BOOT RECORD.								
209				*									
210				XFR	BEGN54					B 937		9	937

SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS
)0J004	0207: 0)1J004	0250: 0)6J004	0110: 0)6K004	0700: 0)6L004	0704: 0)6M004	0728: 0
)8J004	0257: 0)8K004	0273: 0)9J004	0281: 0)9R004	0286: 0	AFMT	0984: 0	BEG52C	0934: 0
BEGN54	0937: 0	CDOVLY	0700: 0	CLR52C	1696: 0	FMTBAS	1697: 0	FMTSW	0696: 0	GMWM	1070: 0
GMWM54	1070: 0	LFMT	0976: 0	LGM	2031: 0	LIMADR	2015: 0	LOAD54	0934: 0	LOADNX	0700: 0
PHAS54	0201: 0	PHASLD	0381: 0	SKIP54	0995: 0	SKIPB	0934: 0	SKIPC	0935: 0	SKIPCD	1032: 0
SKIPD	0936: 0	SKIPTP	1045: 0	SNAPEX	0564: 0	SNAPSH	0333: 0	TPERR	0728: 0	TPREAD	0704: 0

UNREFERENCED SYMBOLS

CLR52C GMWM54 LGM LIMADR PHASLD SKIP54 SNAPEX SNAPSH