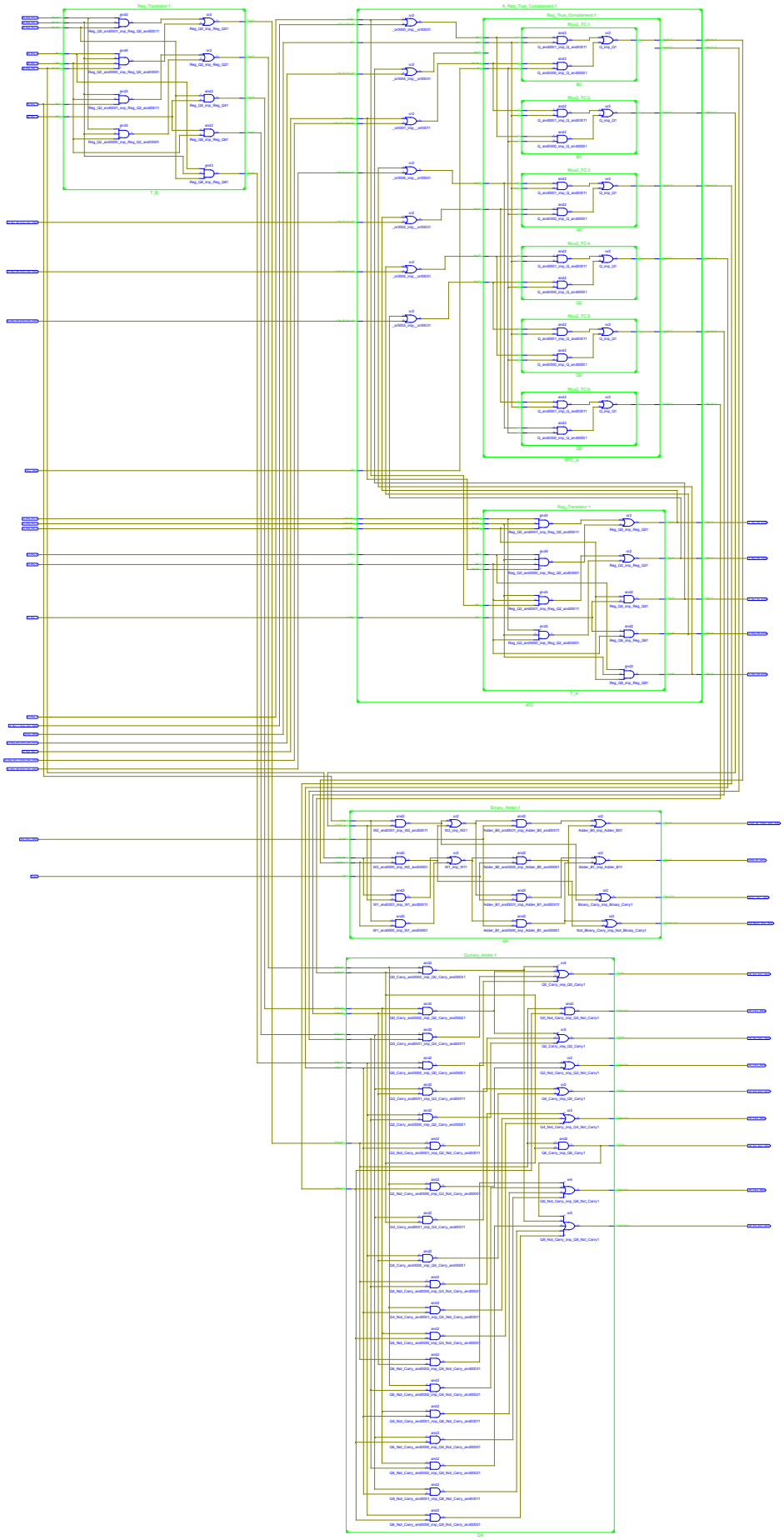
Reg_Translator:1
T_B
A_Reg_True_Complement:1
Reg_True_Complement:1
Mux2_TC:1
B0
Mux2_TC:2
B1
Mux2_TC:3
Q3
Mux2_TC:4
Q2
Mux2_TC:5
Q5
Mux2_TC:6
Q6
RTC_A
Reg_Translator:1
T_A
ATC
Binary_Adder:1
BA
Quinary_Adder:1
QA